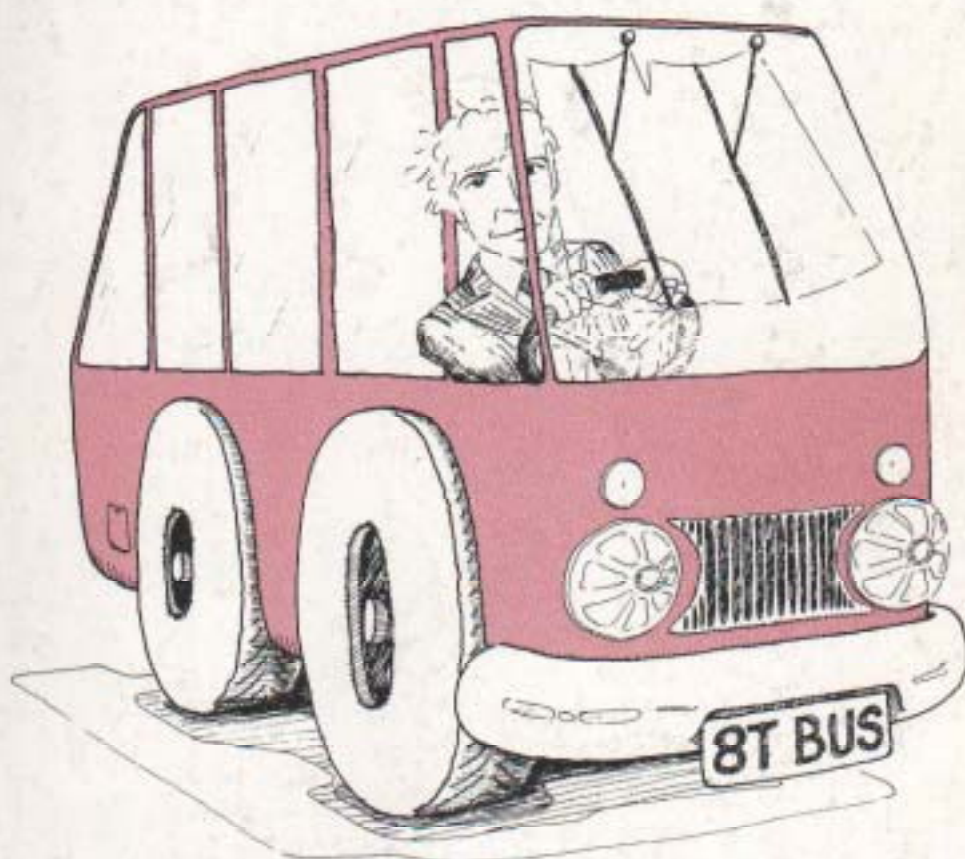


80-BUS NEWS

JANUARY-FEBRUARY 1984

VOL. 3 ISSUE 1

- NASCOM ASSEMBLERS REVIEWED
- POLYDOS FILE UPDATE PROGRAM
- CP/M DIS-ASSEMBLER REVIEWED
- LAWRENCE!



**The Magazine for
NASCOM & GEMINI USERS**

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80-BUS NEWS

Volume 3. Issue 1.

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Letters to the Editor

Please note that occasionally there may be a considerable interval between the receipt of a letter and the time that it materialises in this column. Consequently events that have occurred in the meantime may make the letter out of date. If this is the case we apologise for any inconvenience thus caused.

MBASIC, 16 bit, & other bits

I was pleased to find that at least one 80-BUS user had read my article on disk MBASIC (Letters - 80-BUS News Vol.2, Iss.4).

The version of MBASIC that I use is 5.21 (July 1981) and it really is possible to include comments on the same line as program statements without, as Mr Stuckey suggests, the obligatory colon before the apostrophe. This appears in all manuals I have seen which have been issued after 1977 and applies to both Disk and 'Extended' versions only. It is also available in a number of mainframe BASICs (such as PDP and DEC - which I have used for more than 10 years).

It will be very interesting to see what transpires in the way of high level languages and operating system(s) as support for the promised 80-bus 16 bit CPU card (from Gemini, I understand, although they denied having any such revolutionary ideas a few months ago!). I suspect that Pascal will be available, and possibly Fortran as well (since Prospero Software already have a 16-bit PASCAL available and it would be useful to have a 16-bit version of their excellent implementation of FORTRAN when they get round to it); it would be a pity if some form of BASIC will not be supported, but it seems probable that its use will be confined to 8-bit machines, to die a lingering death. Will we need to spend lots of money on CP/M-86 or will MSDOS be the preferred operating system - and will our 8-bit CP/Ms be totally redundant? What about compatibility with our AVCs, MAP RAM cards and colour cards? I look forward to hearing more about it - price, estimated launch date, etc.

Does anyone actually use COMAL-80? I noticed that Atherton's book was mentioned in the last issue of 80-BUS News - and having used the aforementioned text, I rapidly came to the conclusion that it was better to learn PASCAL and forget that COMAL ever existed since it is, in reality, only a "souped-up" and rather poor subset of BASIC with vaguely Pascal-like structuring. A comparison between interpreted MBASIC and COMAL-80 using the PCW benchmark programs showed that COMAL was significantly slower on almost all the programs apart from BM8 (which involved some work with intrinsic functions) where it was twice as fast as the BASIC, taking 27 seconds to carry out a 1000 times loop with log, sine and exponentiation.

Yours sincerely, P D Coker, Farnborough, Kent

[Ed. - I note from a magazine that Prospero are advertising their Pascal and Fortran in both 8 and 16 bit forms - it is to be hoped that the 16 bit versions are not just code conversions of the 8 bit versions, if this is the case they are likely to be slower!]

Angry at Nascom

I am writing to you to air my anger and frustration at Lucas Logic. I have been a faithful supporter of Nascom for 5 years, but Lucas Logic's absence from the PCW Computer Show was the final straw.

With the launching of two new products - the LX Printer and the LX80 MicroComputer - it was the ideal platform to advertise. Obviously they couldn't give a damn!

Lucas Logic apparently have lost their commitment to Nascom Microcomputers and products. Their relationship with Dealers is appalling.

The sooner Nascom is bought by a decent company the better. In my opinion in their hands Nascom is dead.

Yours in extreme anger and disappointment,
Dr D Plews, MB ChB, Keighley, W Yorks

Rory a Sadist?

Whilst reading Rory O'Farrell's article "Happy Talk", I got the impression that he is some sort of sadist. The time taken to load a file into a disassembler, save it into 4K blocks as a hex file, then finally to transfer the file, must be a least 10 minutes per block.

For some time now, I have been working on a quick and cheap data transfer program for the home enthusiast. The transmitting of data is done via a BASIC program which reads any file type and transmits via the serial port. For reasons mentioned in Rory O'Farrell's article, the program cannot transmit pure binary object code, it is therefore necessary to convert the file to some form that may be recognised by PIP and not to send it Control Zs. The file is received on the second machine via the RDR: into a file. A small BASIC program is then required to convert the program back into its binary form.

Finally, a little tip for NASCOM CP/M users using the AVC board. When most people boot-up CP/M they type AVCTXT straight away to give an 80 column screen, or modify the BIOS via the config program to execute a file AUTO.SUB. You then create the file AUTO.SUB and enter the command AVCTXT.

Have you ever wondered why it takes 15-20 seconds to execute AVCTXT after boot-up? The reason is simple, when Auto load is specified, it sets a jump in the BIOS, the address jumped to is to load SUBMIT.COM, it is then the job of SUBMIT.COM to load AVCTXT and execute it, therefore, taking time loading two files to achieve one goal.

Pondering on this, I decided to delve into MOVCPM. I found the bytes which loaded SUBMIT.COM and changed these to AVCTXT and resaved MOVCPM. CP/M now boots-up AND executes AVCTXT in less than 5 seconds. The trick is:

DDT MOVCPM.COM
- D 0A00

Notice that locations 0A08 - 0A17 contain SUBMIT AUTO.
Change these locations to the following:-

0A07	- This is the length of the command to be executed
0A08 41 A	
0A09 56 V	
0A0A 43 C	
0A0B 54 T	
0A0C 58 X	
0A0D 54 T	
0A0E 2E	

SAVE 41 MOVCPM.COM
MOVE 55 *
SYSGEN
SOURCE (press enter)
DESTINATION B

Yours faithfully, P.A. Dutton, Northfield, Birmingham.

Re:C.B. A.K.A. Dr.D

In a recent hard-copy interchange with that super-programmer, peerless hardware hack and expert wine connoisseur Chris Blackmore, the subject of software exchange loops cropped up. Chris suggested I write to you and deal directly "with the Mafiosi who have a firm grip on the throat of 80-BUS NEWS" concerning non-CP/M exchange loops. Right, here goes.

My N-1 (yes, there are still a few around!) is fitted with a GM805 disk drive and runs D-DOS (boo, hiss, boo), DCS-DOS and POLYDOS. I am currently in disk-to-disk contact with one Alan Wood Esq of St Mellons, Cardiff, using both DCS-DOS and POLYDOS. If an exchange loop exists for either or both of these DOS formats, would you put me (us) in contact with the organisers. Alternatively, if said loops do not exist you could direct any interested parties my way and I'll have a bash at setting a loop (or loops) going, 'like wot yer doc. did for CP/M'. Ah, fame at last.

I will start an exchange loop, and I will also name it unto you. And it will be called TOROID FERRUGINOUS, the greatest software exchange in the history of time and space (or, the biggest anticlimax EVER).

Another point raised in conversation was the inordinately long time taken by 80-BUS to cough up the readies for articles published. Now I know that 99% of the contributors who write to you do so just to see their names in print and/or to help other readers. But please take note, C.B. and D.G.R. DO IT FOR VER MONEY!! Why do you think Chris Blackmore makes home-brew wine? It's because he's too poor to visit the local wine bar, okay, ya. And my Nascom rusts in the corner 'cos, 'cos, I can't afford to pay yer 'lectric bill. And all this just because you employ a disslexic, er, dislexic, um, diss-lexik accountant who doesn't know how to sighn, er sign a cheque!. I'm seriously thinking of trading in my Nascom for an I.B.M. 370!

Happy hacking, D.G. Richards, 29 Martin Crescent, Tonyrefail,
Mid. Glamorgan, South Wales, CF39 8NT.

Re. Easicomp Board

With regard to Mr A Brown's information on the Easicomp Sound Board, in part 2 of your I/O map (Vol.2, Iss.4), please note that, although the manual states that ports 10 and 11 (decimal) are used if NASIO is provided, I find that the board actually decodes ports 130 and 131 (82H and 83H). I assume from this that Easicomp changed the design of their board at some point, and hence boards of both types exist. Incidentally, in common with the PSG design published in INMC 80 News, Issue 5, I now use ports 8 and 9!

Many thanks for printing the "Nascom ROM BASIC Dis-assembled" articles! Although I had dis-assembled this previously, the added documentation is very helpful.

Regarding the contents of your magazine, I feel you are leaving many of your readers behind by printing lengthy articles on very specific topics. This is an unfortunate side-effect of the flexibility of an 80-BUS system. What about including a short questionnaire as part of your subscription form, to get a better idea of what equipment people own?

On a different note, I would be interested in Doctor Dark's program exchange idea, assuming you have had replies from anyone else interested in using tapes. While on the subject of software, I notice that you have been publishing very little software and even fewer software reviews. Is this because no-one is writing programs for the Nascom anymore?

Yours sincerely, Kevin Smith, Aberdeen.

Dealing with RFI

I am glad to see that the problem of RFI is being considered seriously (80-BUS News Vol.2, Iss.5) and delighted to learn that we may get an issue devoted to Amateur Radio. For a starter, may I suggest the following additional hints on the problem of RFI.

- 1) A metal case to enclose your computer is vital. Mine came from a Radio Rally, custom-made out of 16swg steel except for a 1/8th aluminium front panel for #15. This vast box measures 19x14.5x9 inches and leaves a lot of room for all the expansion boards.
- 2) Multi-core screened cable for keyboard, printer and etc, may be obtained from Maplins whose catalogue may be read (and purchased) at W.H.Smiths. Screened cable is a must for stopping RF radiating from lovely aerials dangling from your computer.
- 3) Ferrite rings (try Ambit International) are effective. All leads (power, cassette, tv/monitor) emerging from your case to be wound a few times around the rings, as near as the case as practicable.
- 4) A mains filter (Ambit). This is effective both ways. It stops RF escaping into the mains where the house wiring acts as a massive aerial and prevents the mains crud getting at your computer.
- 5) Desperate measure. In Wireless World, September 1983, G3NRW recommends a 150 pF capacitor between the +12 V rail and ground.
- 6) Another desperate measure. Try earthing all your bits (computer proper, tv/monitor, cassette, etc) at only one point.

RFI appears worst for amateurs at 70 MHz. I achieved a measure of success for that band with some of the above, plus critical computer/radio spacing and complete success by executing HALT! ON 144 MHz, I have a few S1 whistles, but presently the lid is off the box and a Hobbit mechanism dangling on an unscreened lead.

By the way, has anyone made Hisoft Pascal talk to the Hobbit?
Yours, G. Orford, Bristol.

A Lunatic Writes

Dear Dirtbags,

Oldsters, who are, like, total vacuum-heads and do not understand the problems of a young person of today, often get the wrong idea about Waldo 'D.R.' Dobbs, who is like me, man. They think that I have no romance in my soul, which is, like, this incredibly strange thing that I have inside my body. To them I say: "You are totally incorrect, man". It is, like, the total lack of 80-BUS NEWS that causes these sauzzball manifestations, man! Why, when I see these utterly horrible PLASTIC BOXES, I am, like, totally overcome with emotion. There are these worthless diseased MOLLUSCS all over the magazine stands in W.H. Nasties, reading about these disturbingly repulsive micros, man.

Degenerate reptile, man, I ask you to stop this senseless deprivation of REAL COMPUTERS that you are, like, doing to me, man.

Monstrously, hazardously, like, yours, man.

Waldo 'D.R.' Dobbs [No address given.]

Utters from the gutters

By Mick Waters

Before getting down to the meaty bits, I should say that I am one of Dr. Dark's dodos in that I have never before submitted anything for publication. In the past I haven't subscribed regularly to either the INMC or 80-BUS News and used to pick up my copies about once a year on my rare excursions to one of the London dealers. This year I made a resolution to persuade (con) a relative into buying me a subscription as a Christmas present and so now I should be able to read the latest scandal almost as it happens. I don't pretend to be an authority on Nascom hardware but have managed over the past five years or so to become reasonably acquainted with the way they work. Mind you, being an RAF technician, I am an expert on modern electronic techniques (circa 1960) and can write volumes on pentodes, triodes and other glass encapsulated transistors with heaters. Since I have been about 12 months behind everyone else, this article may be too late for solving the problems of a couple of readers who asked for advice as far back as the March/April 83 issue but here goes regardless.

Nascom/SIMON

The first problem(s) of interest came from S. Willmott and from the Sept/Oct 83 issue of Dr. Darks diary. First Dr. Dark, your problem with SIMON getting in the way when verifying a page-mode style RAM-DISK appears to me to be due to the fact that the Nascom computers do not support page mode and SIMON will be present (and generating a RAMDIS signal) no matter what page of RAM is selected. The answer? Read on.

Mr Willmott asked how SIMON may be removed from the system once its job is done. I did (yet) another mod to my N2 as I objected to losing 4k of RAM and also to the prospect of buying a page mode EPROM board for one chip. My solution is not very elegant but it works. Below is a table of advantages and disadvantages which should be weighed up before going any further:

For it

1. Its CHEAP. (Free in fact)
2. It doesn't involve hacking your faithful Nascom around.
3. It works.

Against it

1. The tape drive may no longer be used for anything else.
2. SIMON doesn't work apart from booting the system up as it effectively switches itself out.
3. You must power up with a disk in drive A.
4. Pressing RESET doesn't work any more. The only way to reset is to switch off then switch back on. This creates problems when a program goes into a loop as you can't then RESET and examine the memory.
5. This mod. may only be used if an IVC is fitted as chips are borrowed from the redundant Nascom video circuitry.

In my opinion, the advantages outweigh the disadvantages, you may not think so. If you decide to try this mod, proceed as follows:

1. Pull out the chips used for the video circuitry except IC8.
2. On LKS1 remove all links except for the link between pins 3 and 14.

3. Connect a wire from TP10 to LKS1 pin 4.
4. Remove IC8 and bend pins 1, 2 and 3 (carefully) out straight so that they will clear the socket and replace IC8.
5. Remove IC71 and do the same with pin 8.
6. Connect a wire from IC71/8 to IC8/2.
7. Touch solder a wire onto IC24/12 and connect the other end to IC8/1.
8. Touch solder a wire onto IC18//13 and connect the other end to IC8/3.

The effect of this mod is only to provide a chip select and a RAMDIS signal when the tape drive is on. Certainly on my machine, it is always on when powered up so SIMON is connected in and any RAM in the system occupying those addresses is disabled. Once SIMON is entered other than for booting up or once CP/M is loaded and started, port 0 is reset and SIMON disappears from the memory map. Plug in and power up and all should be as before (on a 60k max CP/M) provided SIMON is not used as a monitor. If the system is powered up without a disk in drive A, your machine will crash once port 0 is reset by SIMON.

Before a full 64k system can be installed, the cold boot loader must be modified to switch out SIMON before loading the CP/M system. To do this, perform the following sequence of operations:

1. Use MOVCPM to generate a 64k system. Allow room for SYS if you use it. Save the result as CPM64.COM.
2. Load CPM64.COM under DDT or ZSID.
3. Use the S command to modify the addresses given below to the values indicated.

090B 18	097C 3E
090C 6C	097D 01
0979 AF	097E 18
097A D3	097F 8D
097B 00	
4. Use ^C or G0 and save the modified CPM64.COM.
5. Use SYSGEN to put the file on drive A. This can be done by typing:
SYSGEN CPM64.COM
after which, specify drive A as the destination.
6. Unplug & plug in again with the new system in the A drive and Hey Presto, you should have a 64K system up and running with no SIMON in the way. As an added bonus, it won't get in the way when paging RAM either.

Faulty RAM Board

My second bit of first-aid is probably of no use to Kevin Weatherhead who has, by now, probably cured his faulty RAM board himself. If not then here goes.

Kevin doesn't say which type of RAM board he is using, so I shall go into the Nascom RAM A & B boards and the Gemini GM802 64k RAM card. The problem is this. When Kevin powers up, his RAM card is full of rubbish as expected. He can write zeros to the card but not ones. Fortunately, all three RAM cards use the same principle so only a brief "how it works" is necessary.

When the Z80 wants to read data, the address lines contain the required address. This address is decoded by the RAM cards to select the appropriate RAM chips. The /WR signal from the Nasbus/80-bus provides the /WR signal to the RAM chips so that they accept data rather than send it. This signal will be a '1' when reading and this side appears to work. As Kevin can also write to the chips, albeit in a limited fashion, this appears to be switching correctly. Data to be written to the RAM chips is gated into the RAM after

buffering by a 74LS244 whose enables are always tied to 0v. When reading, the /RD signal is used to enable a similar LS244 for buffering the data leaving the card. Assuming that the LS244 is faulty, swapping the two buffers should give an indication of whether this is so. If on power up, all of the RAM appears to contain either all ones or all zeros and no attempt to change them succeeds then replace the LS244. A table of chip numbers for all three RAM cards is given below.

LS244's used as write buffers:

RAM A card
IC1

RAM B card
IC26

Gemini G802
IC44

If changing the chip specified doesn't cure the problem then I can only suggest that the board is returned to the manufacturers via your dealer.

CCPZ

The last bit of what appears to be a takeover bid by me concerns three bits of software that should by this time, be available from Henrys. The first item is about CCPZ. As stated by Dave in a previous issue, there is a bug in all versions (with the possible exception of the Gemini release version) up to V4.1. After this version, CCPZ was made available in Macro-80 source code (hooray) and had the bug fix in it together with an implementation of the "." command. Since then, another bug has come to light when using SUBMIT files with the GET command. Try typing:

GET ADDR FILENAME.TYP

DIR A:

DIR B:

If you get the first command repeated three times then you have the bug. This is because CCPZ changes the default DMA address when getting the requested file and forgets to change it back. The result is that further SUBMIT commands are read to memory above the last file read. CCPZ then checks the default buffer and of course finds the last command which is repeated. With version 4.3, this bug has been fixed and there is no reason why everyone shouldn't be using CCPZ now. Order yours tomorrow! (No I don't work for Henry's).

BDOSZ

My second bit of software news concerns a Z80 implementation of the CP/M BDOS, by some strange coincidence, called BDOSZ. In true CCPZ fashion, the space saved by doing the conversion has made room for extra goodies while still retaining CP/M 2.2 compatibility. This package came about as a result of the good fortune of Chris Bellingham of Canterbury, who found a part completed source version of something which would behave rather like the CP/M 2.2 BDOS (author unknown) in an ancient file tucked away in the memory banks of the VAX mainframe at the University of Kent. Chris was at that time running CP/M 1.4 with a second hand Henelec FDC and drives. Being on a students grant, he had never considered the change to CP/M 2.2 as being economically viable. Having found the BDOS and typed the source into his Nascom, there followed a good deal of hard work to finish the BDOS and debug it. Once completed and armed with CCPZ and a home brew BIOS, he had CP/M 2.2 for next to nothing.

At about this time, I obtained a copy for interest value. The error reporting was as Chris originally found it and if possible was worse than that in DRI's BDOS. Also, at the end of the BDOS was a large unused area which we thought might be able to provide some extra goodies. As Chris was busy, I got the job of upgrading BDOSZ to something that would cure the limitations placed on users by the standard CP/M BDOS.

After the usual amount of swearing and cursing, it was finished and working, the results of this are now available at a very modest price.

Now for what it does:

Users of CP/M will have realised by now that the BDOS's error trapping and reporting leave a lot to be desired. How many times have you tried to erase or change a file only to find that either the disk or file is read only? Worse still, if after 6 hours processing, your latest masterpiece tries to update its data file and finds it read only. Unless you have the facility to change the attributes, all of that time has been wasted because you can only reboot, set the file to R/W and start again. Finally, have you got any source files that you would like to assemble, producing listing files to disk, but the PRN file is larger than the capacity of your disk? Infuriating isn't it that you can't just change the disk and carry on?

BDOSZ attempts to overcome these and other problems so that your valuable time is not wasted and your trusty Nascom or Gemini isn't kicked around the floor in temper. An example of each of the BDOS error types and BDOSZ's actions in these cases are given below:

BDOS Err On x: Select

In this case, BDOSZ outputs the message "Drive x: select error" followed on a new line by "Enter valid drive or ^C". The BDOS will then wait for a key press and either log in the selected drive or perform a warm boot as appropriate. This facility is useful if a program being debugged causes a select error and the user wishes to continue testing it.

BDOS Err On x: R/O

In the case of a R/O disk, BDOSZ will print the message "Disk x: is set R/O" followed by "Do it anyway? (Y/N/^C)". If the user types "Y" then the disk will be reset and the function completed as though the error had never existed.

BDOS Err On x: File R/O

Under CP/M, if the command "ERA *.*" is given and one of the files to be deleted is R/O then the above message will be displayed. The CP/M BDOS doesn't tell you which file it is referring to. Under BDOSZ, the message "File FILENAME.TYP is set R/O" followed by the message "Do it anyway? (Y/N/^C)". If the user types "Y", the files R/O attribute bit is cleared and the file will be deleted, renamed or written to as requested as though the file had been R/W. If the user types "N" then the queried file will be left unchanged and still R/O. If, as in the above example, the command ERA *.* was issued, this facility allows the deletion of all R/W files and selected R/O files in the same command.

NOTE. BDOSZ will not query a file unless it is R/O.

BDOS Err On x: Bad Sector

This error is not considered recoverable and most BIOS's these days contain facilities to re-try. If your BIOS can't sort out the problem, then the BDOS has no chance. However, in an attempt to provide meaningful error messages, BDOSZ will display either "Disk x: read error" or "Disk x: write error". In either case, if ^C is pressed, a warm boot will be performed. Pressing any other key will cause the error to be ignored.

Other Errors

Two other errors of a potentially disastrous nature are treated as recoverable by BDOSZ. These are disk directory full and disk full. In the

former case, the BDOS will print the message "Disk x: directory full" and in the latter "Disk x: full". In both cases, the message "Change disks? (Y/N/^C)" will be displayed. If the user types ^C, the system will perform a warm boot. If "N" is typed, a standard "disk full" or "directory full" code will be returned to the CCP or calling program. If "Y" is typed, in the case of a full disk, the currently addressed file will be closed. In either case, when the BDOS is ready to proceed, it will display the message "Change disks then hit any key or ^C". If the user types ^C, the system will be rebooted. If not then a new disk should be in the current drive and BDOSZ will reset the new disk to make it R/W, log it in, create a new file on this disk with the same name as before and continue with the write. The calling program will not be aware of the disk change. BDOSZ will erase any file of the same name occurring on the replacement disk unless it is R/O. In this case, BDOSZ will query before deleting.

As standard, BDOSZ sends a BEL character (ASCII 7) to the console device for those users with a bleep facility to warn the user that an error has occurred.

BDOSZ has been in use now for about 12 months on three different machines. No bugs have been found to date and the extra facilities provided have proved invaluable.

[Ed. - a warning. Owners of Gemini systems with BIOSs of version 2.8 or greater beware! These BIOSs do a check on power-up to determine if, and how many (up to 4) Gemini GM833 'RAM-DISK' boards are present. If any are present the BIOS copies the CCP and BDOS to drive 'M' and modifies the BDOS to use this for warm boots. Whether this will work with BDOSZ or not, who knows?]

MDIS

The last piece of this software trilogy is for those of you tempted by Henrys adverts for MDIS. For those who haven't seen the ad., MDIS is a CP/M disassembler with differences. It includes all of the excellent features found in David Parkinson's NAS-DIS and then some. The last version of MDIS to be released was 2.1, version 2.2 was substituted after a small unexpected "feature" was discovered. But now, version 2.3 is available with oodles of extras. [Ed. - Since the time of writing, vers. 2.6 has appeared.] To those who haven't yet obtained a copy, you were possibly right to wait. For those who have, never mind, upgrades are available at the cost of a copy charge. Simply return it to the dealer you bought it from. Being somewhat biased in favour of MDIS, I wouldn't attempt to review it but will provide a list of its features (note that features in this case isn't in quotes).

1. MDIS produces either Z80 or 8080 mnemonics as requested by the user but will default to the mnemonics used by the CPU in use.
2. Assembler source files may be produced. Current versions provide 100% compatibility with the Microsoft Macro-80 assembler.
3. Listing files may be directed to the CON:, PUN: or LST: devices or may be sent to a disk file.
4. Labels are produced automatically. Labels are a four digit hex number related to the address where the label is to be inserted. To make the labels assembler compatible, they are given an alphabetic prefix. One of four prefixes will be used for each label depending on whether MDIS thinks the label refers to code, data, both or doesn't know.
5. A cross-reference listing may be supplied which lists each label and each address where the label is used.
6. Allows data areas to be specified as either hex bytes, ASCII, an address table (with labels substituted for addresses) or a look-up table in the form:

byte
address
byte
address

Again, labels will be substituted for addresses in look-up tables.

7. On-screen editing using the cursor controls is available on all line-inputs to MDIS.
8. MDIS works with XSUB. (One in the eye for DISZILOG).
9. Allows tables of data area addresses and types to be redirected to come from a text file on disk. This facility may be combined with input under XSUB.
10. Screen paging is available if required but not to SYS standards. (Who needs both?)
11. MDIS allows printed listings to be stopped after each page so that single sheet paper may be changed.
12. Margins may be specified on all output listings so that mounting in ring binders is possible. A unique feature enables alternate pages to have margins so that if single sheet paper is in use, both sides may be used.
13. Listings are formatted so that data areas are separated from code by a blank line to improve readability. Additionally, blank lines are also inserted after JP (ss) instructions and any unconditional jump, jump relative or return instructions.
14. MDIS decodes NAS-SYS, restarts correctly.
15. Disassembles right up to FFFFH and will not overflow to 0, unlike many other disassemblers.
16. MDIS allows selected portions of the object program to be disassembled (a useful subroutine for example) and will produce a stand alone program for reassembly.
17. MDIS permits the user to enter titles and subtitles which will be used on page headings or after the appropriate pseudo-ops on Macro-80 source files.
18. The user is allowed to define his own edit keys (even on systems without user defineable keys on the keyboard).
19. MDIS will accept a printer initialisation string to be entered for ease of use.
20. A user subroutine area has been included for those special initialisation jobs (like redefining the numeric keypad as cursor controls on the Rotec keyboard - a routine to do this is provided free).
21. MDIS works on ANY system running CP/M-80 [properly!!! Superbrains don't like it. - Ed.].

At #50, MDIS surpasses all other CP/M disassemblers in both performance and price. Too good to be true? Most of the Microvalue Dealers will provide a free demonstration to callers on request. Finally, those of you who have versions 2.1 or 2.2 will know that MDIS was protected against disassembling itself and against copying to other systems. In spite of the possibility of rip-offs, version 2.3 has had these checks removed for two reasons. Firstly, it was causing problems for some dealers where some computer manufacturers didn't serialise their CP/M systems properly and second, I was feeling hypocritical as how do you think I obtain those pieces of software that I don't write for myself.

DOCTOR DARK'S DIARY - EPISODE 20.

Well, I never thought all those centuries ago that I would end up writing this many articles for the magazine! I think I owe you all thanks for putting up with me for so long, and to show you that you should not have done, here is my latest load of waffle.

I read the other day, somewhere or other, that the Nascom 1 is alive and well, and costs \$50 in kit form from Lucas. You can't keep a good machine down, and that's a fact. You tell the kids of today you had to pay over \$200 for one, and they'll laugh in your face...

Another thing I chanced upon, in a weekly publication called "Computing", was a review of Boris Allan's book about Logo. At the bottom of the page was a picture of lots of school children in a room with lots of Nascom 3's. It was nice to see that all the claims made by Acron about their monopoly are not 100% true after all. Apparently, Nasnet works. I have tried to use Acron's Ekonet (the names have been changed so that I won't be sued by the guilty!) but it is slower than first class post, when more than two people are using files on the same day!

And now, down to serious stuff, probably not a moment too soon! The Pluto graphics board is short of decent software, unless the people who are writing it don't want it reviewed, and are advertising it in something I don't read. Before you can get round to writing any really big programs for the thing, you need some sort of standard software interface to it. The one that follows is not quite complete, and one day I will get round to writing the definitive version. I have tried several times to write something like this, and have learned several interesting things from my failures along the way. My first versions were in assembler, and very effective, but I wanted something on a higher level, for ease of programming. The first attempt in Pascal was going to have a separate procedure or function for each of the functions of the Pluto board itself. As the set of routines grew longer, I kept compiling them, to check for errors. At a certain stage, the HiSoft Pascal 4 compiler became angry, and would not compile the end of the program. It works well enough with longer programs made up of fewer routines, so I can only assume that a stack somewhere is filling up, or maybe it is a table of addresses that gets too full? Anyway, it was a very ugly program, so I began again with a different approach.

What I really wanted to be able to do was write something along the lines of "pluto(plot,42,42)", in which the name of the required routine and the necessary parameters were passed in the manner shown. But not all of the routines take the same number of parameters. And some return values, while others do not. Pascal does not provide a construction that will let you do anything so variable. So the version that follows is given only the routine name in the call, and a set of global variables that mimic the Pluto board's internal variables must be set to the values you would have passed as parameters, had that been possible in Pascal. All clear? I thought not. If you want to set the current colour to red, you have to write:

```
ccol := red;
pluto(sccol)
```

This is more or less friendly to use, but not by any means perfect! If anyone has any good ideas about how to improve matters, I for one would be glad to see them.

I know it is not as pretty as it should be, but it does work. At least, those parts that don't say "not yet written" work! Any comments will be read with interest, and offers of Pluto Palette boards to test the next version with will be met with considerable grovelling.

```

PROGRAM pluto;
CONST
  {Pluto port addresses.}
  status = 160; data = 161;
  {Pluto routine numbers.}
  alloep = 163; arc = 193; bfill = 176; bfills = 177;
  bfillsp = 179; clrcwp = 172; copy = 133; copyts = 132;
  ffill = 130; ffillp = 174; ffills = 173; ffillsp = 175;
  fbccl = 144; fbcclp = 147; fcp = 150; fcdp = 148;
  fscp = 146; fscwp = 143; fcol = 143; fpat = 183;
  ipcol = 187; irsel = 185; istat = 134; istyle = 142;
  itcol = 145; iwprot = 149; limage = 159; limagc = 188;
  liner = 157; liners = 158; lineto = 128; lsym = 160;
  lsync = 189; mover = 152; movers = 153; moveto = 151;
  pinit = 166; plot = 154; plotr = 155; plotsr = 156;
  rfill = 129; rimage = 161; rimagc = 190; rpxc = 167;
  rpxr = 168; rpxrs = 169; rsym = 162; rsymc = 191;
  sbcol = 135; sccol = 137; scdp = 131; scsp = 164;
  scwp = 165; sfcol = 136; sfpatr = 180; sfpatr = 181;
  shires = 170; slores = 171; spat = 182; spcol = 186;
  srsel = 184; sstyle = 138; stcol = 139; swprot = 140;
  {Pluto colours.}
  black = 0; green = 1; blue = 2; cyan = 3;
  red = 4; yellow = 5; magenta = 6; white = 7;
  {Pluto pixel block maximum sizes, to set maximum size
  of the array used by pixel load and save routines.}
  pixwide = 10; {Or other value.}
  pixhigh = 10; {Or other value.}
TYPE
  {Pluto colour type.}
  plutocol = black..white;
  {Pluto subrange types.}
  byte = 0..255;
  xcoord = 0..639;
  ycoord = 0..287;
  xinc = -639..639;
  yinc = -287..287;
  xincsh = -127..128;
  yincsh = -127..128;
  horiz = 1..640;
  vert = 1..288;
VAR
  {Pluto colour variables.}
  ccol, bcol, fcol, tcol, pcol : plutocol;
  {Pluto partition and workspace variables.}
  cwp : 1..2; {Current working partition.}
  csp : 0..255; {Current symbol partition.}
  cdp : 1..2; {Current display partition.}
  cpx : 0..639; {Current position, X value.}
  cpy : 0..287; {Current position, Y value.}
  {Pluto miscellaneous variables.}
  stat : byte; {Pluto status variable.}
  pat : 0..255; {Line and arc pattern.}
  {Single colour plane operators.}
  wprot : 0..7; {Write protect mask.}

  rsel : 0..7; {Read select mask.}
  {The style variable.}
  style : 0..255;
  {Pluto parameter variables.}
  X : xcoord; {Absolute X, Y coordinates.}
  Y : ycoord;
  DX : xinc; {16 bit X, Y displacements.}
  DY : yinc;
  dx : xincsh; {8 bit X, Y displacements.}
  dy : yincsh;
  width : horiz; {Width and height.}
  height : vert;
  n : byte; {General 8 bit number.}
  p : byte; {Partition identifier.}
  c : plutocol; {General colour returned.}
  {Parameters for the copy routine.}
  xfrom, xto : xcoord;
  yfrom, yto : ycoord;
  pfrom, pto : byte;
  {Parameters for the arc routine.}
  arcxc, arcyc : INTEGER;
  arcxe : xinc;
  arcy : yinc;
  {Parameter block for the pixel block commands.}
  pixels : ARRAY [1..pixhigh,1..pixwide] OF plutocol;
  PROCEDURE sendword(w1, w2 : INTEGER);
  {This sends two 16 bit values to the data port
  address of the Pluto board. As I am using the
  HSA-88B board and HiSoft Pascal 5, it has to do
  a bit of messing round to get the 16 bits we
  want from the 32 actually stored!}
  BEGIN
    OUT(data, PEEK(ADDR(w1)+2, CHAR));
    OUT(data, PEEK(ADDR(w1)+3, CHAR));
    OUT(data, PEEK(ADDR(w2)+2, CHAR));
    OUT(data, PEEK(ADDR(w2)+3, CHAR));
  END;
  PROCEDURE sendxdy;
  BEGIN
    OUT(data, CHR(dx)); OUT(data, CHR(dy))
  END;
  PROCEDURE setdefaults;
  BEGIN
    ccol := 7; bcol := 0; fcol := 7; tcol := 7;
    pcol := 7; cwp := 1; csp := 255; cdp := 1;
    X := 0; Y := 0; pat := 240; wprot := 0;
    rsel := 7; style := 128
  END;
  PROCEDURE pwait;
  {This waits until the Pluto board is ready.}
  BEGIN
    WHILE INP(status) < CHR(128) DO {nothing}
    END;
  PROCEDURE notyet;
  BEGIN

```

```

WRITELN('Called routine is not yet written!');
HALT
END;
{
And now the actual routine!
-----
}
PROCEDURE pluto(com : byte);
VAR
  i, j : INTEGER;
BEGIN
  pwait; {Wait until Pluto is ready.}
  {Send Pluto command.}
  OUT(data,CHR(com));
  {Now send parameters, if any.}
  CASE com OF
    alloop : BEGIN pwait; sendword(width,height);
              OUT(data,CHR(n)) END;
    arc : BEGIN pwait; sendword(arcxc,arcc);
           sendword(arcxc,arcc) END;
    copy : BEGIN pwait; sendword(width,height);
           OUT(data,CHR(pfrom));
           sendword(xfrom,yfrom);
           OUT(data,CHR(pto));
           sendword(xto,yto) END;
    copyts : BEGIN pwait; OUT(data,CHR(m)) END;
    limage : BEGIN pwait; sendword(width,height);
             FOR i := 1 TO height DO
               FOR j := 1 TO width DO
                 OUT(data,CHR(pixels[i,j])) END;
               OUT(data,CHR(pixels[i,j])) END;
             END;
    limage : BEGIN pwait; notyet END;
    liner : BEGIN pwait; sendword(OX,DY) END;
    liners : BEGIN pwait; sendword(OX,DY) END;
    lneto : BEGIN pwait; sendword(X,Y) END;
    lsym : BEGIN pwait; OUT(data,CHR(m));
           FOR i := 1 TO height DO
             FOR j := 1 TO width DO
               OUT(data,CHR(pixels[i,j])) END;
             OUT(data,CHR(pixels[i,j])) END;
           END;
    lsymc : BEGIN pwait; notyet END;
    mover : BEGIN pwait; sendword(OX,DY) END;
    movers : BEGIN pwait; sendword(OX,DY) END;
    moveto : BEGIN pwait; sendword(X,Y) END;
    pinlt : BEGIN pwait; setdefaults END;
    plot : BEGIN pwait; sendword(X,Y) END;
    plotr : BEGIN pwait; sendword(OX,DY) END;
    rfill : BEGIN pwait; sendword(OX,DY) END;
    rfill : BEGIN pwait; sendword(width,height) END;
    rimage : BEGIN pwait; sendword(width,height) END;
    rimage : BEGIN pwait; sendword(width,height) END;
    rpix : BEGIN pwait; sendword(X,Y) END;
    rpixr : BEGIN pwait; sendword(OX,DY) END;
    rpixrs : BEGIN pwait; sendword(X,Y) END;
    rsym : BEGIN pwait; notyet END;
    rsymc : BEGIN pwait; notyet END;
    sbcol : BEGIN pwait; OUT(data,CHR(bcol)) END;
  END;
END;
BEGIN {MAIN PROGRAM}
{You have to write this part for yourself, of
course, but it should be a lot easier now!}
END.

```

```

sccol : BEGIN pwait; OUT(data,CHR(ccol)) END;
scdp : BEGIN pwait; OUT(data,CHR(cdp)) END;
scsp : BEGIN pwait; OUT(data,CHR(csp)) END;
scwp : BEGIN pwait; OUT(data,CHR(cwp)) END;
sfcol : BEGIN pwait; OUT(data,CHR(fcol)) END;
sfpatr : BEGIN pwait; sendword(width,height);
          OUT(data,CHR(p));
          sendword(X,Y) END;
sfpats : BEGIN pwait; OUT(data,CHR(p));
          OUT(data,CHR(n)) END;
spat : BEGIN pwait; OUT(data,CHR(pat)) END;
spcol : BEGIN pwait; OUT(data,CHR(pcol)) END;
srsl : BEGIN pwait; OUT(data,CHR(rsel)) END;
sstyle : BEGIN pwait; OUT(data,CHR(style)) END;
stcol : BEGIN pwait; OUT(data,CHR(tcol)) END;
swprot : BEGIN pwait; OUT(data,CHR(wprot)) END;
END;
{Now get return values if any.}
CASE com OF
  alloop : BEGIN pwait; p := ORD(INP(data)) END;
  fbccl : BEGIN pwait; bcol := ORD(INP(data)) END;
  fccol : BEGIN pwait; ccol := ORD(INP(data)) END;
  fcp : BEGIN pwait; X := ORD(INP(data))+256*ORD(INP(data));
        Y := ORD(INP(data))+256*ORD(INP(data)) END;
  fcdp : BEGIN pwait; cdp := ORD(INP(data)) END;
  fcsdp : BEGIN pwait; csp := ORD(INP(data)) END;
  fcwp : BEGIN pwait; cwp := ORD(INP(data)) END;
  ficol : BEGIN pwait; fcol := ORD(INP(data)) END;
  fipat : BEGIN pwait; pat := ORD(INP(data)) END;
  fipcol : BEGIN pwait; pcol := ORD(INP(data)) END;
  firsl : BEGIN pwait; rsel := ORD(INP(data)) END;
  fistat : BEGIN pwait; stat := ORD(INP(data)) END;
  istyle : BEGIN pwait; style := ORD(INP(data)) END;
  itcol : BEGIN pwait; tcol := ORD(INP(data)) END;
  iwprot : BEGIN pwait; wprot := ORD(INP(data)) END;
  rimage : BEGIN pwait;
            FOR i := 1 TO height DO
              FOR j := 1 TO width DO
                pixels[i,j] := ORD(INP(data)) END;
              pixels[i,j] := ORD(INP(data)) END;
            END;
            rsymc : BEGIN pwait; notyet END;
            END;
            rpix : BEGIN pwait; c := ORD(INP(data)) END;
            rpixr : BEGIN pwait; c := ORD(INP(data)) END;
            rpixrs : BEGIN pwait; c := ORD(INP(data)) END;
            rsym : BEGIN pwait;
                  FOR i := 1 TO height DO
                    FOR j := 1 TO width DO
                      pixels[i,j] := ORD(INP(data)) END;
                    pixels[i,j] := ORD(INP(data)) END;
                  END;
                  rsymc : BEGIN pwait; notyet END;
                  END;
            END;
            BEGIN {MAIN PROGRAM}
            {You have to write this part for yourself, of
            course, but it should be a lot easier now!}
            END.

```

CP/M File Transfer Program Via The IEEE488 Bus.

By S. Wood

Below is a simple program written in BASIC-80 which, if present on two machines fitted with the EV Computing IEEE488 interface, will allow data files to be transferred between them at a reasonable rate. The speed loss is due to the amount of data conversion done in BASIC, but it is still faster than serial interface methods previously used. Shortly a machine code version with more features will be made available which will be able to transfer files very fast indeed (approaching 200 K bytes a second). The speed limitation then will be in the disk drives and not in the software or interface.

```

10 REM 488IO, Written by S.Wood. March 1984
20 WIDTH 255
30 B$=CHR$(2)
40 REM SENDER IS ADDRESS 1 & SYSTEM CONTROLLER
50 REM RECEIVER IS ADDRESS 2
60 INPUT "S)END OR R)ECEIVE";A$
70 IF A$="R" OR A$="r" THEN 390
80 IF A$="S" OR A$="s" THEN 110
90 IF A$<>" " THEN PRINT CHR$(7);:GOTO 60
100 END

110 REM SEND SECTION
120 PRINT B$;"pon,1/s":REM Initialise Sender as system controller
130 INPUT "File name";F$
140 PRINT B$;"wrt,2 ";F$:REM Send filename to be loaded across
150 OPEN "R",1,F$,128
160 R=1:FIELD 1,128 AS X$
170 GET 1,1:REM Force dummy file read otherwise EOF function does not work
180 WHILE NOT EOF(1):REM Main sending loop
190 GET 1,R
200 R=R+1
210 S1$="":S2$="":REM Generate two ASCII-Hex strings 128 and 129 bytes long
220 FOR A=1 TO 64:REM the second string has an EOF marker attached.
230 S$=HEX$(ASC(MID$(X$,A,1))):IF LEN(S$)=1 THEN S$="0"+S$
240 S1$=S1$+S$
250 NEXT A
260 FOR A=65 TO 128
270 S$=HEX$(ASC(MID$(X$,A,1))):IF LEN(S$)=1 THEN S$="0"+S$
280 S2$=S2$+S$
290 NEXT A
300 IF EOF(1) THEN S$="A" ELSE S$="M"
310 S2$=S2$+S$
320 PRINT B$;"wrt,2 ";S1$
330 PRINT B$;"wrt,2 ";S2$
340 WEND
350 PRINT"Complete."
360 INPUT "Any more files to send";A$
370 IF A$="y" OR A$="Y" THEN RUN
380 END

390 REM RECEIVE SECTION
400 PRINT B$;"pon,2":REM Power-on the receiver as address 2
410 PRINT B$:"INPUT "red,1";FR$
420 PRINT"File ";FR$;" is being received."
430 R=1
440 OPEN "r",1,FR$,128
450 FIELD 1,128 AS X$
460 REM RE-ENTER HERE
470 TEMP$=""
480 PRINT B$:"INPUT "red,1";S1$
490 PRINT B$:"INPUT "red,1";S2$:TEMP$=""
500 IF RIGHT$(S2$,1)="A" AND LEN(S2$)=129 THEN 610:REM END OF FILE
510 FOR A=1 TO 128 STEP 2
520 TEMP$=TEMP$+CHR$(VAL("&H"+MID$(S1$,A,2)))
530 NEXT A
540 FOR A=1 TO 128 STEP 2
550 TEMP$=TEMP$+CHR$(VAL("&H"+MID$(S2$,A,2)))
560 NEXT A
570 LSET X$=TEMP$
580 PUT 1,R=R+1
590 IF RIGHT$(S2$,1)="M" THEN 460:REM Not EOF yet
600 IF RIGHT$(S2$,1)<>"A" THEN PRINT"error":STOP
610 CLOSE 1
620 PRINT B$;"rdy":REM Read last line-feed from Bus
630 RUN

```

Dave Hunt's Bits

So 80-BUS is on the move again, and has caught me by totally by surprise as I've nothing ready to print. Do I hear cries of shame!!! [Ed. - No.]

They say that behind every great man is a woman calling the shots, and in the case of our editor, this seems to be the case. Now he's too modest and/or shy to make any comment, but if I say that shortly before the Christmas issue was due to be put to bed, a rather nice young lady entered his life suffice to say that the demands on his spare time (usually devoted to magazine preparation) was devoted, or, should I say, diverted elsewhere. Now, this being the case, if you assume that the reappearance of the mag. is indicative of a departure in his life, you would be wrong, perhaps he's feeling guilty having left his readership magazine-less for so long, or something; whatever, it has created a great spurt of energy, and not only is this issue in the last stages of preparation as I write, but the next issue as well.

The radio bits first....

Firstly AMTOR, I am now in possession of a number of reports on the development of the commercial system upon which AMTOR is based and also the various specifications which comprise the working details of AMTOR. My grateful thanks to those readers (several anonymous) who sent me the details. I confess that I'm somewhat intrigued by the two copies addressed directly to my home, as at the time my address hadn't been published anywhere (now QTHR in the 1984 callbook). Sadly, I'm not sure that I will be able to make use of the information (although all information is ultimately useful at sometime). As you may have guessed I have been investigating 'PACKET RADIO', or at least the bastardized form that the UK amateur radio regulations allow.

Literally a couple of days ago I was presented with the PACKET RADIO program by G8WJL and G6GIX, for the BBC computer. I swiped a BEEB and had a go, it certainly works well. I particularly liked the fact that no additional hardware was required, just a lead which plugs from the BEEB tape I/O socket into the mic, ptt and extension speaker sockets of the rig.

Despite the ease of setting up, I feel the program suffers from a number of minor deficiencies, niggles really. If the program is active, then it will read any packet information that it sees, so if there are two or three QSO's going on on the same frequency (don't forget the idea of packet is to allow just this), then the program writes the packets to the screen. This is fine if you are 'earwiggling' and just want to know who's around. The problem is that if you start a QSO with a station, then (as far as I can see) this reporting of other QSO's continues, making it difficult to see who is talking to who. Worse, because of this reporting, it suggests that multiway QSO's are possible, fine until you try it. As a data anti-collision protocol is used with random timing between packets, there is no knowing which station is going to send first, so unless 'to' -> 'from' callsigns are included in each packet text then multiway QSO's becomes extremely confusing. A simple software toggle to turn the 'blurb' off once contact is established would be a very good idea.

A further off-shoot of the 'blurb' reporting is the ability to 'sort of' send a CQ, something which packet does not normally allow. If you program the originating callsign as your own and the destination callsign as CQCQ rather

than a legit callsign, followed by the message 'DE G6MFR', then any station in the 'earwiggling' mode will see:

G6MFR: CQCQ DE G6MFR

The receiving station, say G2XXX, can then open a communications channel to you simply by programming the destination callsign as the callsign just received. So his channel becomes G2XXX -> G6MFR. G2XXX can then send a message (or even no message), say KN DE G2XXX, where upon you will see:

G2XXX: KN DE G2XXX

What's the problem? Well, as you were sending to a mythical station called CQCQ you can't go back to G2XXX until the packet program gives up trying to contact the station CQCQ (16 tries over the space of a couple of minutes), or without first having escaped from the program and then reRUNning it. This takes time and clears the screen so if you've got a bad memory like me, then you've forgotten who was calling in the first place.

The program needs to know when no-one else is transmitting to allow the anti-collision logic to work. This is done by manually opening the squelch and the consequent white noise is detected by the tape I/O (probably as random numbers) indicating that the program is free to transmit. When a strong blank carrier is up, or data is being received, the program inhibits transmit and goes to sleep for a random period before trying again. Now this has snags. A noisy incoming signal which doesn't quite trigger the tape I/O could be ignored or interpreted as a free channel allowing transmit (this sort of error occurs on signals worse than about 4 by 5). Secondly, white noise must be present to indicate the channel is free. This precludes any phase lock signal shaping circuits which could be used to reconstitute a noisy signal, thereby much improving the sensitivity of the system. Now the rig squelch circuit is much better at differentiating between signal and no signal than the computer, so a logical development would be to return the squelch line to the computer to detect a channel free condition. For sideband use, a phase lock shaper could be used and the out-of-lock line from this may be used to indicate a free channel.

... and BBC BASIC

It was to investigate my niggles and with a mind to convert the program to my machine that I started to poke around inside the program. Now I haven't paid much attention to the BEEB machine in the past, preferring my machine which I consider superior and, at least, I understand. Some people tell me the BEEB is the most fantastic machine since the invention of the rocket propelled roller skate, whilst others tell me that all BEEBs should be collected, along with their owners, put in a large box and dumped at the bottom of the Marianas Trench as an insidious danger to mankind. I know of one gentleman who made a considerable loss by trading in an almost new Gemini MultiBoard machine for a BEEB, whilst I know of another who can't wait to trade his BEEB for a Galaxy when he can rake up the necessary. So overall, on a statistical sample of two, 50% of BEEB owners are loonies whilst the other 50% have seen the light and should be helped as much as possible. Certainly the BEEB seems to me to be overpriced and has gained a sort of cult acceptance which it does not deserve, but then the same could be said for some of the other machines around, so ... there you go!!

Anyway on to BEEB 'BASIC', the quotes are deliberate, as whatever it is, it bears little relation to the original Dartmouth College Basic. In fact its closer relations would seem to be some sort of cross between Pascal and C with BASIC syntax. Perhaps a better name for it might be BEEB C-BasPas, anything, but not BASIC. As a language, it's very powerful, but encourages 'opaque programming' under the guise of 'structured programming'. In other words if the programmer cares to do the job properly with lashings of comment and everything laid out in a nice orderly fashion, then anything written in C-BasPas should be beautifully clear; on the other hand, if the programmer wanted make his program difficult to read (or is lazy like most of us), all he has to do is remove all the comment, jumble the order of the PROCedures and function calls, and the program becomes as clear as mud. The latter is more likely to be the case as the available memory in the BEEB is quite small (having subtracted the screen memory and sundry workspaces (a lot of which don't seem to do much)), as it's quite difficult to squeeze a program of any size into the available memory space without resorting to 'dirty' short cuts.

The BEEB is also provided with a large number of indirectly addressed operating system calls (driven from a number table, something like NAS-SYS) which the user is encouraged to use from the C-BasPas. The OS calls are reasonably documented in the manual, but rely on an understanding of the hardware and devices used, information which is sadly not provided. (I understand the Advanced User's manual guide goes into detail, but as the BEEB machine I have is on borrow, I don't feel inclined to buy this just to see what's happening).

The program uses the BEEB CUTS tape I/O but seems to address it directly, as the data sent appears to be in the form of a bit stream (as it should be) rather than ASCII characters with start and stop bits using the UART as I had hoped. Shades of nasty undocumented things going on in the ULA. All this will prove an interesting exercise on a Gemini, as I guess a couple of bits from a port are going to have to be hard wired to the tape I/O and a software UART written, either that or I'm going to have to design an SIO piggyback board.

So my original plan of quickly rewriting the PACKET program into something more generalized has had to go by the board. It's not so much an exercise in rewriting, but having to learn C-BasPas and the detail innards of the BEEB operating system and hardware, a time wasting exercise that I'm not too keen on. Oh well, I will carry on (unless someone else wants to have a go). Instead of something I judged would take a few evenings to do, it looks like it's going to take weeks. Still when (if) I've done it we'll publish it here so that Gemini's and Nascom's can take to the air in the PACKET mode.

As to my opinion of the BEEB after a couple of days acquaintance ... well let's say I wouldn't give more than fifty quid for one if someone was rash enough to pass one my way!! Torch have the right idea, demote the BEEB to a terminal for use on a CP/M system, it's about all it's good for. Pity Torch had to do their own thing with a non-standard CP/M system. Perhaps I might have a go at doing the same with a MultiBoard system, because overpriced as the BEEB is, it might just be cheaper than buying an SVC and keyboard; it could then replace these two, with high res. colour thrown in. Now all this brands me as a heretic with the BEEB cult. So I'd better keep quiet otherwise some dark night, someone might find me lying in a dark alley with a BEEB in my back.

BIOS 3 ... Where is it?

Some time ago (in fact about this time last year, early June) I wrote a piece called 'SYS is dead, long live ...', where I stated that due to alleged piracy of the SYS disk drive source for commercial purposes Gemini were likely to withdraw their permission for the publication of their disk drivers in the SYS source. At about the same time Gemini started to charge very high prices for their source code to bona fide users. The point of my piece was that Gemini were pricing their source code at prices which only pirates, who could take commercial advantage of their source code, could afford. This accusation on my part quite understandably upset Gemini, who it was aimed at, to the point that they have threatened to produce a product called BIOS 3 which will allow the Gemini user to update his BIOS to the latest spec. and include all Gemini permutations of drive/controller/video for himself. As the majority of SYS users were using SYS for just this purpose, the need for SYS would disappear. So far BIOS 3 has not appeared, so a gentle nudge is required. How about it Gemini!!!

An apology

Much to my surprise, my piece 'SYS is dead, long live ...' provoked a much more vehement reaction from an unexpected quarter. Due to the tendency of 80-BUS magazine to appear in two's, my published comments in Vol.2 Iss.4 could be linked with an unattributed editorial piece (which I hasten to add I did not write) in Vol.2 Iss.5, the following issue, which appeared about the same time. MAP80 Systems of Chertsey have taken very strong objection to these two pieces and claim that these have damaged their business. If MAP80 Systems think that my piece was directed at them and in consequence, damaged their business, then I can only apologise and unreservedly withdraw any such imputation. My piece made no allusions direct or indirect to any specific concern.

As far as the second piece, the editorial, is concerned, this was merely a statement of fact, nothing more nothing less. The details of the litigation were sub-judice at the time and as far as I know, still are. The fact that MAP80 Systems and Gemini are in litigation has no bearing on my piece, as this litigation started sometime after I wrote the piece, which after all, as stated above, was intended as a 'side-swipe' at Gemini.

The SVC Board

The new Gemini GM832 Super Video Controller Board has at long last escaped from Amersham. I'm not sure whether this was a mistake or not, as up to a week or two ago, these cards were rare-er than rocking horse manure (very good for plastic rose trees I'm told). Anyway one of these cards fell into my clutches for a couple of hours before being whipped away by the customer who had paid for it months before.

You don't get much time to do a full evaluation in two hours, but first impressions were good. There was none of the video patterning characteristic of the GM812 when in inverse video. The most immediate impression is speed. Using the TYPE command under CP/M revealed a staggering increase in speed with text hurtling up the screen at a phenomenal rate. A lot more than the 50% increase directly attributable to the use of a 6MHz Z80B. The screen/workspace RAM contention logic has been removed from software and placed in hardware, and this is one other factor increasing the speed. The nett result looks more like 150% faster, although I didn't set to with stop watch to time it or to read the documentation to see if any actual figure is quoted. The only problem

with this improved display speed is that fingers 2 and 4 on my left hand had to increase their reaction time by a proportional amount, as there was barely time to bash ^S before the thing you wanted to see disappeared off the screen. Perhaps a ZZzzz... command could be built into CCPZ to slow it up when not actually executing a program.

The next feature investigated (for its novelty) was the clock display. Once enabled, a changing clock display is written at a user predetermined cursor position on the screen. The default cursor position is the extreme top right hand corner of the screen, and this suits most software I had a chance to try it with. The internal clock generator is interrupt driven from the frame sync., which is divided by 50 to update the clock counter and redisplay the clock once a second. As the system clock is crystal controlled, it is accurate enough for most purposes although I think reprogramming the 6845 video controller could upset the clock accuracy. A very simple routine was written which read the time from the GM822 RTC and loaded the clock on the SVC. At the end of the two hour period there was a discrepancy of 7 seconds between the RTC and the SVC clock.

A number of bits of software which were known to be to greater or lesser extent video card dependant were tried to see if the compatibility between the GM832 and the now discontinued GM812 were as claimed, and all were found to work as before. DISKPEN/GEMPEN required adjustment to the cursor speed patch byte, because the cursor flash rate went berserk, but in all other respects except the 48 wide mode (the new SVCs second screen mode is now 40 wide) DISKPEN/GEMPEN worked well. The increased speed of the card showed to advantage with WORDSTAR, screen rewrites being accomplished with very acceptable speed. My old CHARGEN program for reprogramming the character sets worked, but of course needs rewriting in the light of the SVC as there are now two programmable character sets (total 256 characters) instead of one.

The SVC has a number of new features including 256 by 256 pixel graphics with built in line and circle drawing software, selectable attributes for flashing characters with low, half and inverse intensities, inputs for serial keyboards, and many other things. As I said, I only laid hands on one for a very short period, so it is very difficult to be objective. I didn't find any nasty quirks and the board behaved impeccably. Perhaps now Gemini will loan me one to play with, and I'll be able to find out what it can really do. The only thing that worries me though is the very high degree of sophistication of this card, commensurate with its elevated price. In my experience, 99% of users were unaware of the potential of the old GM812 card, so is all this cleverness really necessary?

The Climax colour card

The Climax colour card is to be re-introduced. Gemini have acquired the rights to manufacture the card from Climax, and deliveries are to commence very shortly. This card was very popular with those owning it, and some very clever displays have been seen. But as the card has been unavailable for the best part of a year, interest in it has waned. With its welcome reappearance perhaps some new and clever software will start to be written. Certainly as the video format of the Climax card is the same as the high-res mode of the new SVC, there will be demand for a piece of software which will act as an interface between the two. Here is an opportunity for CC-SOFT, who have written some rather nice graphics software for Gemini gear in the past to come up with something. It would be nice to see two interfaces with common inputs

which would then either drive the SVC or the Climax from the same graphics programs.

With the advent of either black and white high-res or colour graphics perhaps some enterprising software writers will have a go at writing some arcade type games for the Gemini and Nascom hybrid machines. These would have to be written for fun as there would be little money in it, but it is surprising how often we get asked about games for the Gemini. There are a number of the more intellectual type, Adventure, Chess, Planetfall, etc., but the arcade types tend to get overlooked. I know I personally have no patience with arcade games, both lacking in the necessary co-ordination to play them and also in the necessary patience to practice. I also realise for the cost of a Climax card (now Gemini GM837) I could buy a Spectrum and a whole bag full of Space Invaders tapes. But some people seem to want them and where there is a need someone will usually try to fill it.

Qwikdraw

Whilst still on the subject of colour graphics, the expensive Pluto card (now only available in the full 8MHz, extended monitor form) has gained a very versatile graphics package shortly to be made available by Gemini. Called Qwikdraw, and written primarily for use on the Gemini networks installed in the Manpower Services YTS training scheme, Qwikdraw is an easy to use graphics package with some very novel features. Input is either from the keyboard (using the cursor keys with selectable step rates) or, ultimately, from a bit pad. Drawing of graphical displays is quite easy with automatic circle and smoothed curve creation. Block and complex shape fills and colour floods are also catered for. An optional colour mixing package will allow up to 32 colours to be displayed, and a 'picture compiler' which can convert the stored picture format into a .COM file for immediate execution. Limited animation is also possible. Display is normally to a high resolution colour monitor or to a dot matrix printer or to a multicolour plotter. Very extensive 'help' facilities are provided, which are an education in themselves with animated graphics demonstrating the points queried. One interesting use demonstrated was the preparation of the cels used for overhead projectors in an educational environment. The cels being drawn on the plotter using oil based pens. Whilst not as versatile as the Nascom Lotti, it is extremely quick and easy to use with a powerful editor, it should find lots of applications outside the YTS scheme particularly in the education field.

Printer. RS232 interface. 180 cps. Bidirectional. Buffered. True Descenders. #80. Crawthorne (0344) 776894.

Nascom 2 with Nas-Sys 3, Naspen, 48K RAM and Castle interface - #300; additional 48K RAM B - #75; self-contained keyboard with lead - #35; graphics chip - #10; PSU - #25; Bricomp Real Time Clock/Calendar - #17; Nasbus EPROM/ROM card with Naspen, Debug, ZEAP - #45. 0532 740921

Colour card (Holmes/R&EW) - 16 colour + sprites. Also has 2 sound generators, 2 clock chips, 8 port A-D, CMOS RAM, CTC. Fully built, working with hardware and software. All ic's (socketed) except 1 off AY-3-8910. All xtals and backup battery. Also interface card to convert colour diff. signals to RGB, offers around #100. Tel C. Bowden 0209 860480 for details.

NASCOM

ROM

BASIC

DIS--ASSEMBLED

PART 5

BY CARL LLOYD--PARKER

Dis-assembly of NASCOM ROM BASIC Ver 4.7

F012 E5	ARLDV: PUSH HL	; Save code string address
F013 F5	PUSH AF	; A = 00 , Flags set = Z,N
F014 2AD810	LD HL,(VAREND)	; Start of arrays
F017 3E	DEFB (LD A,n)	; Skip "ADD HL,DE"
F018 19	ENDARY: ADD HL,DE	; Move to next array start
F019 EB	EX DE,HL	
F01A 2ADA10	LD HL,(AREND)	; End of arrays
F01D EB	EX DE,HL	; Current array pointer
F01E 0D8AE6	CALL CPDEHL	; End of arrays found?
F021 CA4AF0	JP Z,CREARY	; Yes - Create array
F024 7E	LD A,(HL)	; Get second byte of name
F025 B9	CP C	; Compare with name given
F026 23	INC HL	; Move on
F027 022CF0	JP NZ,NXTARY	; Different - Find next array
F02A 7E	LD A,(HL)	; Get first byte of name
F02B B3	CP B	; Compare with name given
F02C 23	INC HL	; Move on
F02D 5E	LD E,(HL)	; Get LSB of next array address
F02E 23	INC HL	
F02F 56	LD D,(HL)	; Get MSB of next array address
F030 23	INC HL	
F031 0218F0	JP NZ,FNDARY	; Not found - Keep looking
F034 3AAC10	LD A,(LCRFLG)	; Found - Locate or Create it?
F037 B7	OR A	
F038 02B6E3	JP NZ,DDERR	; Create - ?DD Error
F03B F1	POP AF	; Locate - Get number of dim'ns
F03C 44	B,H	; EC Points to array dim'ns
F03D 4D	C,L	
F03E CA54F7	JP Z,POPHRT	; Jump if array load/save
F041 96	SUB (HL)	; Same number of dimensions?
F042 CAASFO	JP Z,FINDEL	; Yes - Find element
F045 1E10	E,BS	; ?BS Error
F047 C3C1E3	JP ERROR	; Output error

```

FO4A 110400      CREATY: LD  DE,4
FO4D F1         POP  AF
FO4E CAA0E9     JP   Z,FOERR
FO51 71         LD   HL,(HL),C
FO52 23         INC  HL
FO53 70         LD   HL,(HL),B
FO54 23         INC  HL
FO55 4F         LD   C,A
FO56 CDBAE3     CALL CHKSTK
FO59 23         INC  HL
FO5A 23         INC  HL
FO5B 22C510     LD   HL,(CUOPR),HL
FO5E 71         LD   HL,(HL),C
FO5F 23         INC  HL
FO60 3AAC10     LD   A,(LCRFLG)
FO63 17         RLA
FO64 79         LD   A,C
FO65 010B00     CRARLP: LD  BC,10+1
FO68 D26DFO     JP   NC,DEFSIZ
FO6B C1         POP  BC
FO6C 03         INC  BC
FO6D 71         LD   HL,(HL),C
FO6E 23         INC  HL
FO6F 70         LD   HL,(HL),B
FO70 23         INC  HL
FO71 F5         PUSH AF
FO72 E5         PUSH HL
FO73 CDFFF8     CALL MLDEBC
FO76 EB         EX   DE,HL
FO77 E1         POP  HL
FO78 F1         POP  AF
FO79 3D         DEC  A
FO7A C265F0     JP   NZ,CRARLP
FO7D F5         PUSH AF
FO7E 42         LD   B,D
FO7F 4B         LD   C,E
FO80 EB         EX   DE,HL
FO81 19         ADD  HL,DE
FO82 DAA2E3     JP   C,OMERR
FO85 CD95E3     CALL ENFMEM
FO88 22DA10     LD   (ARREND),HL

```

```

FO8B 2B         ZERARY: DEC  HL
FO8C 3600       LD   (HL),0
FO8E CDBAE6     CALL CPDEHL
FO91 C28F0      JP   NZ,ZERARY
FO94 03         INC  BC
FO95 57         LD   D,A
FO96 2AC510     LD   HL,(CUOPR)
FO99 5E         LD   E,(HL)
FO9A EB         EX   DE,HL
FO9B 29         ADD  HL,HL
FO9C 09         ADD  HL,BC
FO9D EB         EX   DE,HL
FO9E 2B         DEC  HL
FO9F 2B         DEC  HL
FOA0 73         LD   HL,(HL),E
FOA1 23         INC  HL
FOA2 72         LD   HL,(HL),D
FOA3 23         INC  HL
FOA4 F1         POP  AF
FOA5 DACCF0     C,ENDDIM
FOA8 47         FINDEL: LD  B,A
FOA9 4F         LD   C,A
FOAA 7E         LD   A,(HL)
FOAB 23         INC  HL
FOAC 16         DEFB (LD D,n)
FOAD E1         FINDEL: POP HL
FOAE 5E         LD   E,(HL)
FOAF 23         INC  HL
FOB0 56         LD   D,(HL)
FOB1 23         INC  HL
FOB2 E3         EX   (SP),HL
FOB3 F5         PUSH AF
FOB4 CDBAE6     CALL CPDEHL
FOB7 D245F0     JP   NC,BSERR
FOBA E5         PUSH HL
FOBB CDFFF8     CALL MLDEBC
FOBE D1         POP  DE
FOBF 19         ADD  HL,DE
FOC0 F1         POP  AF
FOC1 3D         DEC  A
FOC2 44         LD   B,H
FOC3 4D         LD   C,L
FOC4 C2ADF0     JP   NZ,FNDELP
FOC7 29         ADD  HL,HL
FOC8 29         ADD  HL,HL
FOC9 C1         POP  BC
FOCA 09         ADD  HL,BC
FOCB EB         EX   DE,HL
FOCC 2AD010     ENDDIM: LD  HL,(NXTOPR)
FOCF 09         RET

```

```

; Back through array data
; Set array element to zero
; All elements zeroed?
; No - Keep on going
; Number of bytes + 1
; A=0
; Get address of array
; Number of dimensions
; To HL
; Two bytes per dimension size
; Add number of bytes
; Bytes needed to DE
; Save LSB of bytes needed
; Save MSB of bytes needed
; Locate / Create?
; A is 0, End if create
; Find array element
; Number of dimensions
; Skip "POP HL"
; Address of next dim' size
; Get LSB of dim'n size
; Get MSB of dim'n size
; Save address - Get index
; Save number of dim'ns
; Dimension too large?
; Yes - ?BS Error
; Save index
; Multiply previous by size
; Index supplied to DE
; Add index to pointer
; Number of dimensions
; Count them
; MSB of pointer
; LSB of pointer
; More - Keep going
; 4 Bytes per element
; Start of array
; Point to element
; Address of element to DE
; Get code string address

```

Dis-assembly of NASCOM ROM BASIC Ver 4.7	PAGE 57	PAGE 58
F0D0 2ADA10 LD HL,(AREND) F0D3 EB EX DE,HL F0D4 210000 LD HL,0 F0D7 39 ADD HL,SP F0D8 3AAD10 LD A,(TYPE) F0DB B7 OR A F0DC CAECFO JP Z,FRENUM F0DF CD53F3 CALL GSTRUC F0E2 CD53F2 CALL GARBCE F0E5 2A5A10 LD HL,(STRSPC) F0E8 EB EX DE,HL F0E9 2AC310 LD HL,(STREOT) F0EC 7D FRENUM: LD A,L F0ED 93 SUB E F0EE 4F LD C,A F0EF 7C LD A,H F0F0 9A SBC A,D F0F1 41 ACPASS: LD B,C F0F2 50 ABPASS: LD D,B F0F3 1B00 LD E,0 F0F5 21AD10 LD HL,TYPE F0F8 73 LD (HL),E F0F9 0690 LD B,80H+16 F0FB C32AF8 JP RETINT F0FE 3AAB10 POS: LD A,(CURPOS) F101 47 PASSA: LD B,A F102 AF XOR A F103 C32F0 JP ABPASS F106 CD99F1 DEF: CALL CHEKFN F109 CD7BF1 CALL IDTEST F10C 0170EA LD BC,DATA F10F C5 PUSH BC F110 D5 PUSH DE F111 CD90E6 CALL CHKSYN F114 28 DEFB "(" F115 CD2DEF CALL GETVAR F118 E5 PUSH HL F119 EB EX DE,HL F11A 2B DEC HL F11B 56 LD D,(HL) F11C 2B DEC HL F11D 5E LD E,(HL) F11F E1 POP HL F11F CD44ED CALL TSTNUM F122 CD90E6 CALL CHKSYN F125 29 DEFB ")" F126 CD90E6 CALL CHKSYN F129 B4 DEFB ZEQUAL F12A 44 LD B,H F12B 4D LD C,L F12C E3 EX (SP),HL F12D 71 LD (HL),C F12E 23 INC HL F12F 70 LD (HL),B	F133 CD89F1 CALL DOPN: F136 D5 PUSH DE F137 CD09EE CALL EVLPAR F13A CD44ED CALL TSTNUM F13D E3 EX (SP),HL F13E 5E LD E,(HL) F13F 23 INC HL F140 56 LD D,(HL) F141 23 INC HL F142 7A LD A,D F143 B3 OR E F144 CAB9E3 JP Z,UPERR F147 7E LD A,(HL) F148 23 INC HL F149 66 LD H,(HL) F14A 6F LD L,A F14B E5 PUSH HL F14C 2ADE10 LD HL,(FNRGNM) F14F E3 EX (SP),HL F150 22DE10 LD HL,(FNRGNM),HL F153 2AE210 LD HL,(FNARG+2) F156 E5 PUSH HL F157 2AE010 LD HL,(FNARG) F15A E5 PUSH HL F15B 21E010 LD HL,FNARG F15E D5 PUSH DE F15F CD6BF8 CALL FPTHIL F162 E1 POP HL F163 CD41ED CALL GETNUM F166 2B DEC HL F167 CD36B8 CALL GETCHR F16A C2ADE3 JP NZ,SNERR F16D E1 POP HL F16E 22E010 LD HL,(FNARG),HL F171 E1 POP HL F172 22E210 LD HL,(FNARG+2),HL F175 E1 POP HL F176 22DE10 LD HL,(FNRGNM),HL F179 E1 POP HL F17A C9 RET F17B E5 IDTEST: PUSH HL F17C 2A5C10 LD HL,(LINEAT) F17F 23 INC HL F180 7C LD A,H F181 B5 OR L F182 E1 POP HL F183 C0 RET NZ F184 1E16 LD E,ID F186 C301E3 JP ERROR	; Make sure FN follows ; Save function pointer address ; Evaluate expression in "(" ; Make sure numeric result ; Save code str, Get FN ptr ; Get LSB of FN code string ; Get MSB of FN code string ; And function DEFINED? ; No - ZUF Error ; Get LSB of argument address ; Get MSB of argument address ; HL = Arg variable address ; Save it ; Get old argument name ; Save old, Get new ; Set new argument name ; Get LSB, NLSB of old arg value ; Save it ; Get MSB, EXP of old arg value ; Save it ; HL = Value of argument ; Save FN code string address ; Move FPREG to argument ; Get FN code string address ; Get value from function ; DEC 'cos GETCHR INCs ; Get next character ; Bad character in FN - Error ; Get MSB, EXP of old arg ; Restore it ; Get LSB, NLSB of old arg ; Restore it ; Get name of old arg ; Restore it ; Restore code string address ; Save code string address ; Get current line number ; -1 means direct statement ; Restore code string address ; Return if in program ; ?ID Error

Dis-assembly of NASCOM ROM BASIC Ver 4.7

Dis-assembly of NASCOM ROM BASIC Ver 4.7

```

F189 CD90E6  CHKSYN      ; Make sure FN follows
F18C A7       ZFN      ; "FN" token
F18D 3E80     A,80H
F18F 32CB10   LD      (FORFLG),A
F192 B6       OR      (HL)
F193 47       LD      B,A
F194 CD32EF   CALL  GTFNAM
F197 C344ED   JP      TSTNUM

F19A CD44ED   STR:    CALL  TSTNUM
F19D CDB8F9   CALL  NUMASC
F1A0 CDC8F1   CALL  CRTST
F1A3 CD53F3   CALL  GSTRCU
F1A6 C0AEF3   LD      BC,TOPPOOL
F1A9 C5       PUSH    BC

F1AA 7E       SAVSTR: LD      A,(HL)
F1AB 23       INC     HL
F1AC 23       INC     HL
F1AD E5       PUSH    HL
F1AE CD29F2   CALL  TESTR
F1B1 E1       POP     HL
F1B2 4E       LD      C,(HL)
F1B3 23       LD      HL
F1B4 46       LD      B,(HL)
F1B5 CDC2F1   CALL  CRTMST
F1B8 E5       PUSH    HL
F1B9 6F       LD      L,A
F1BA CD46F3   CALL  TOSTRA
F1BD D1       POP     DE
F1BE C9       RET

F1BF CD29F2   MKTMST: CALL  HL,TWMPSTR
F1C2 21BF10   CRTMST: LD      HL
F1C5 E5       PUSH    HL
F1C6 77       LD      (HL),A
F1C7 23       INC     HL
F1C8 23       INC     HL
F1C9 73       LD      (HL),E
F1CA 23       INC     HL
F1CB 72       LD      (HL),D
F1CC E1       POP     HL
F1CD C9       RET

F1CE 2B       CRTST:  DEC     HL
F1CF 0622     QTSTR:  LD      B,""
F1D1 50       LD      D,B
F1D2 E5       DTSTR:  PUSH    HL
F1D3 0EFF     LD      C,-1
F1D5 23       QTSTLP: INC     HL
F1D6 7E       LD      A,(HL)
F1D7 0C       INC     C
F1D8 B7       OR      A
F1D9 CAE4F1   JP      Z,CRTSTE
F1DC BA       CP      D
F1DD CAE4F1   JP      Z,CRTSTE
F1DE B8       CP      B
F1E1 C2D5F1   JP      NZ,QTSTLP
F1E4 FE22     CRTSTE: CP      ""
F1E6 CC36B8   CALL  Z,GETCHR
F1E9 E3       EX      (SP),HL
F1EA 23       INC     HL
F1EB EB       EX      DE,HL
F1EC 79       LD      A,C
F1ED CDC2F1   CALL  CRTMST
F1F0 11BF10   TSTOPL: LD      DE,TWMPSTR
F1F3 2AB110   LD      HL,(TWMPSTR)
F1F6 22E410   LD      (FPREG),HL
F1F9 3E01     LD      A,1
F1FB 32AD10   LD      (TYPE),A
F1FE CD6EF8   CALL  DETHL4
F201 CD8AE6   CALL  CFDEHL
F204 22B110   LD      HL,(TWMPSTR),HL
F207 E1       POP     HL
F208 7E       LD      A,(HL)
F209 C0       RET     NZ
F20A 1E1E     LD      E,ST
F20C C3C1E3   JP      ERROR

F20F 23       PRNUMS: INC     HL
F210 CDCEF1   PRS:    CALL  CRTST
F213 CD53F3   PRS1:   CALL  GSTRCU
F216 CD62F8   CALL  LOADFP
F219 1C       INC     E
F21A 1D       PRSLP:  DEC     E
F21B C8       RET     Z
F21C 0A       LD      A,(BC)
F21D CD9BE6   CALL  OUTC
F220 FE0D     CP      CR
F222 CC86EB   CALL  Z,DONULL
F225 03       INC     BC
F226 C31AF2   JP      PRSLP

```

```

F229 B7 TESTR: OR A
F22A 0E (LD C,n)
F22B F1 GRBDON: POP AF
F22C F5 PUSH AF
F22D 2A5A10 LD HL,(STRSPC)
F230 EB EX DE,HL
F231 2AC310 LD HL,(STRBOT)
F234 2F CPL
F235 4F LD C,A
F236 06FF LD B,-1
F238 09 LD HL,BC
F239 23 INC HL
F23A CDBA86 CALL CPDEHL
F23D DA47F2 JP C,TESTOS
F240 22C310 LD HL,(STRBOT),HL
F243 23 INC HL
F244 EB EX DE,HL
F245 F1 POPAF: POP AF
F246 C9 RET

```

```

; Test if enough room
; No garbage collection done
; Garbage collection done
; Save status
; Bottom of string space in use
; To DE
; Bottom of string area
; Negate length (Top down)
; -Length to BC
; BC = -ve length of string
; Add to bottom of space in use
; Plus one for 2's complement
; Below string RAM area?
; Tidy up if not done else err
; Save new bottom of area
; Point to first byte of string
; Address to DE
; Throw away status push

```

```

F247 F1 TESTOS: POP AF
F248 1E1A LD E,OS
F24A CAC1E3 JP Z,ERROR
F24D BF CP A
F24E F5 PUSH AF
F24F 012BF2 LD BC,GRBDON
F252 C5 PUSH BC
F253 2AAF10 GARBGE: LD HL,(LSTRAM)
F256 22C310 GARBLP: LD HL,(STRBOT),HL
F259 210000 LD HL,0
F25C E5 PUSH HL
F25D 2A5A10 LD HL,(STRSPC)
F260 E5 PUSH HL
F261 21B310 LD HL,TWSTPL
F264 EB EX DE,HL
F265 2AB110 LD HL,(TWSTFT)
F268 EB EX DE,HL
F269 CDBA86 CALL CPDEHL
F26C 0164F2 LD BC,GRBLP
F26F C2B8F2 JP NZ,STPOOL
F272 2AD610 LD HL,(PROGND)
F275 EB EX DE,HL
F276 2ADB10 LD HL,(VAREND)
F279 EB EX DE,HL
F27A CDBA86 CALL CPDEHL
F27D CAB8F2 JP Z,ARRLP
F280 7E LD A,(HL)
F281 23 INC HL
F282 23 INC HL
F283 B7 OR A
F284 CDB8F2 CALL STRADD
F287 C375F2 JMP SMPVAR

```

```

; Garbage collect been done?
; 70S Error
; Yes - Not enough string space
; Flag garbage collect done
; Save status
; Garbage collection done
; Save for RETURN
; Get end of RAM pointer
; Reset string pointer
; Flag no string found
; Get bottom of string space
; Save bottom of string space
; Temporary string pool
; Temporary string pool pointer
; Temporary string pool done?
; Loop until string pool done
; No - See if in string area
; Start of simple variables
; End of simple variables
; All simple strings done?
; Yes - Do string arrays
; Get type of variable
; "S" flag set if string
; See if string in string area
; Loop until simple ones done

```

```

F28A C1 F28A C1 GNBARY: POP BC
F28B EB EX DE,HL
F28C 2ADA10 ARRLP: LD HL,(ARREND)
F28F EB EX DE,HL
F290 CDBA86 CALL CPDEHL
F293 CA81F2 JP Z,SCREND
F296 CD62F8 CALL LOADFF
F299 7B LD A,E
F29A E5 PUSH HL
F29B 09 LD HL,BC
F29C B7 OR A
F29D F28AF2 JP P,GNBARY
F2A0 22C510 LD HL,(CUOPR),HL
F2A3 E1 POP HL
F2A4 4E LD C,(HL)
F2A5 0600 LD B,0
F2A7 09 ADD HL,BC
F2A8 09 ADD HL,BC
F2A9 23 INC HL
F2AA EB EX DE,HL
F2AB 2AC510 LD HL,(CUOPR)
F2AF EB EX DE,HL
F2B2 CDBA86 CALL CPDEHL
F2B5 01AAF2 LD BC,GRBARY
F2B8 C5 STPOOL: PUSH BC
F2B9 F680 OR SOH
F2BB 7E STRADD: LD A,(HL)
F2BC 23 INC HL
F2BD 23 INC HL
F2BE 5E LD E,(HL)
F2BF 23 INC HL
F2C0 56 LD D,(HL)
F2C1 23 INC HL
F2C2 F0 RET P
F2C3 B7 OR A
F2C4 C8 RET Z
F2C5 44 LD B,H
F2C6 4D LD C,L
F2C7 2AC310 LD HL,(STRBOT)
F2CA CDBA86 CALL CPDEHL
F2CD 60 LD H,B
F2CE 69 LD L,C
F2CF D8 RET C
F2D0 E1 POP HL
F2D1 E3 EX (SP),HL
F2D2 CDBA86 CALL CPDEHL
F2D5 E3 EX (SP),HL
F2D6 E5 PUSH HL
F2D7 60 LD H,B
F2D8 69 LD L,C
F2D9 D0 RET NC
F2DA C1F1F1 POP BC,AF,AF
F2DD E5 PUSH HL
F2DE D5 DE PUSH DE
F2DF C5 PUSH BC
F2E0 C9 RET

```

```

; Scrap address of this array
; End of string arrays
; All string arrays done?
; Yes - Move string if found
; Get array name to EODE
; Get type of array
; Save address of num of dim'ns
; Start of next array
; Test type of array
; Numeric array - Ignore it
; Save address of next array
; Get address of num of dim'ns
; BC = Number of dimensions
; Two bytes per dimension size
; Plus one for number of dim'ns
; Get address of next array
; Is this array finished?
; Yes - Get next one
; Loop until array all done
; Save return address
; Flag string type
; Get string length
; Get LSB of string address
; Get MSB of string address
; Not a string - Return
; Set flags on string length
; Null string - Return
; Save variable pointer
; Bottom of new area
; String been done?
; Restore variable pointer
; String done - Ignore
; Return address
; Lowest available string area
; String within string area?
; Lowest available string area
; Re-save return address
; Restore variable pointer
; Outside string area - Ignore
; Get return, Throw 2 away
; Save variable pointer
; Save address of current
; Put back return address
; Go to it

```


F382 0101F1	LEN:	LD	BC,PASSA	: To return integer A	F3B2 CD37F4	LEFT:	CALL	LFRGNM	: Get number and ending ")"
F385 C5		PUSH	BC	: Save address	F3B5 AF		XOR	A	: Start at first byte in string
F386 CD50F3	GETLEN:	CALL	GETSTR	: Get string and its length	F3B6 E3	RIGHT1:	EX	(SP),HL	: Save code string,Get string
F389 AF		XOR	A		F3B7 4F		LD	C,A	: Starting position in string
F38A 57		LD	D,A	: Clear D	F3B8 E5	MID1:	PUSH	HL	: Save string block address
F38B 32AD10		LD	(TYPE),A	: Set type to numeric	F3B9 7E		LD	A,(HL)	: Get length of string
F38E 7E		LD	A,(HL)	: Get length of string	F3BA B8		CP	B	: Compare with number given
F38F B7		OR	A	: Set status flags	F3BB DAC0F3		JP	C,ALLFOL	: All following bytes required
F390 C9		RET			F3BE 78			A,B	: Skip "LD C,0"
					F3BF 11	ALLFOL:	LD	C,0	: First byte of string
F391 0101F1	ASC:	LD	BC,PASSA	: To return integer A	F3C0 0E00		PUSH	BC	: Save position in string
F394 C5		PUSH	BC	: Save address	F3C2 C5		CALL	TESTR	: See if enough string space
F395 CD86F3	GTFILNM:	CALL	GETLEN	: Get length of string	F3C3 CD29F2		POP	BC	: Get position in string
F398 CA0E9		JP	Z,FCERR	: Null string - Error	F3C6 C1		POP	HL	: Restore string block address
F39B 23		INC	HL		F3C7 E1		PUSH	HL	: And re-save it
F39C 23		INC	E,(HL)	: Get LSB of address	F3C8 E5		INC	HL	
F39D 3E		LD	H,(HL)		F3CA 23		INC	HL	
F39E 23		INC	HL		F3CB 46		LD	B,(HL)	
F39F 56		LD	D,(HL)	: Get MSB of address	F3CC 23		INC	HL	: Get LSB of address
F3A0 1A		LD	A,(DE)	: Get first byte of string	F3CD 66		LD	H,(HL)	
F3A1 C9		RET			F3CE 68		LD	L,B	: Get MSB of address
					F3CF 0600		LD	B,0	: HL = address of string
F3A2 3E01	CHR:	LD	A,1	: One character string	F3D1 09		ADD	HL,BC	: BC = starting address
F3A4 CDBFF1		CALL	MKTMTST	: Make a temporary string	F3D2 44		LD	B,H	: Point to that byte
F3A7 CD87F4		CALL	MAKINT	: Make it integer A	F3D3 4D		LD	C,L	: BC = source string
F3AA 2AC110		LD	HL,(TMPSTR+2)	: Get address of string	F3D4 CDC2F1		CALL	CKTMST	: Create a string entry
F3AD 73		LD	(HL),E	: Save character	F3D7 6F		LD	L,A	: Length of new string
F3AE C1	TOPPOOL:	POP	BC	: Clean up stack	F3D8 CD46F3		CALL	TOSTRA	: Move string to string area
F3AF C3F0F1		JP	TSTOPL	: Temporary string to pool	F3DB D1		POP	DE	: Clear stack
					F3DC CD57F3		CALL	GSTRDE	: Move to string pool if needed
					F3DF C3F0F1		JP	TSTOPL	: Temporary string to pool
						RIGHT:			
F3E2 CD37F4					F3E2 CD37F4		CALL	LFRGNM	: Get number and ending ")"
F3E5 D1					F3E5 D1		POP	DE	: Get string length
F3E6 D5					F3E6 D5		PUSH	DE	: And re-save
F3E7 1A					F3E7 1A		LD	A,(DE)	: Get length
F3E8 90					F3E8 90		SUB	B	: Move back N bytes
F3E9 C3B6F3					F3E9 C3B6F3		JP	RIGHT1	: Go and get sub-string

```

F3EC EB      MID:      EX      DE,HL      ; Get code string address
F3ED 7E      LD        A,(HL)      ; Get next byte ", " or ")"
F3EE CD3CF4  CALL      MIDNUM      ; Get number supplied
F3F1 04      INC        B          ; Is it character zero?
F3F2 05      DEC        B          ; Yes - Error
F3F3 CAA0E9  JP        Z,FCERR      ; Save starting position
F3F6 CA      PUSH      B          ; All of string
F3F7 1EFF    LD        E,255      ; Any length given?
F3F9 FE29    CP        C          ; No - Rest of string
F3FB CA05F4  JP        Z,RSTSTR     ; Make sure ", " follows
F3FE CD90E6  CALL      CHKSYN      ; Get integer 0-255
F401 2C      DEF      B          ; Make sure ")" follows
F402 CD84F4  CALL      GETINT      ; Restore starting position
F405 CD90E6  CALL      CHKSYN      ; Get string, save code string
F408 29      DEF      B          ; Continuation of MID$ routine
F409 F1      POP       AF          ; Save for return
F40A E3      EX        (SP),HL      ; Starting position-1
F40B 01B8F3  LD        BC,MID1        ; Compare with length
F40E C5      PUSH      BC          ; Zero bytes length
F410 BE      DEC        C          ; Null string if start past end
F411 0600    CP        B,0        ; Save starting position-1
F413 D0      LD        NC         ; Subtract start
F414 4F      LD        C,A        ; Enough string for it?
F415 7E      LD        A,(HL)     ; Save maximum length available
F416 91      SUB        C          ; Truncate string if needed
F417 BB      CP        E          ; Set specified length
F418 47      LD        B,A        ; Go and create string
F419 D8      RET        C          ; Get length of string
F41A 43      LD        B,E        ; Result zero
F41B C9      RET                ; Save length

F41C CD86F3  VAL:      CALL      GETLEN ; Get length of string
F41F CA33F6  JP        Z,RESZER      ; Result zero
F422 5F      LD        E,A        ; Save length
F423 23      INC        HL        ; Get LSB of address
F424 23      INC        HL        ; Get MSB of address
F425 7E      LD        A,(HL)     ; HL = String address
F426 23      INC        HL        ; Save string address
F427 66      LD        L,A        ; Get end of string+1 byte
F428 6F      LD        L,A        ; Zero it to terminate
F429 E5      PUSH      HL,DE      ; Save string end,get start
F42A 19      ADD        HL,DE     ; Save end+1 byte
F42B 46      LD        B,(HL)     ; Get starting byte
F42C 72      LD        (HL),D     ; Convert ASCII string to FP
F42D E3      EX        (SP),HL    ; Restore end+1 byte
F42E C5      PUSH      BC        ; Restore end+1 address
F42F 7E      LD        A,(HL)     ; Put back original byte
F430 CD1AF9  CALL      ASCIIFP    ;
F433 C1      CALL      BC        ;
F434 E1      POP       POP        ;
F435 70      LD        HL        ;
F436 C9      RET                ;

```

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F437 EB      LFRGNM: EX      DE,HL      ; Code string address to HL
F438 CD90E6  CALL      CHKSYN      ; Make sure ")" follows
F43B 29      DEF      B          ; Get return address
F43C C1      MIDNUM: POP      DE      ; Get number supplied
F43D D1      POP      DE          ; Re-save return address
F43E C5      PUSH      BC        ; Number to B
F43F 43      LD        B,E        ;
F440 C9      RET                ;

F441 CD87F4  INP:      CALL      MAKINT ; Make it integer A
F444 323F10  LD        (INPRT),A    ; Set input port
F447 CD3E10  CALL      INPSUB      ; Get input from port
F44A C301F1  JP        PASSA        ; Return integer A

F44D CD71F4  POUT:     CALL      SETIO  ; Set up port number
F450 C30610  JP        OUTSUB      ; Output data and return

F453 CD71F4  WAIT:     CALL      SETIO  ; Set up port number
F456 F5      PUSH      AF          ; Save AND mask
F457 1E00    LD        E,0        ; Assume zero if none given
F459 2B      DEC        HL        ; DEC 'cos GETCHR INCs
F45A CD36E8  CALL      GETCHR      ; Get next character
F45D CA67F4  JP        Z,NOXOR     ; No XOR byte given
F460 CD90E6  CALL      CHKSYN      ; Make sure ", " follows
F463 2C      DEF      B          ;
F464 CD84F4  CALL      GETINT      ; Get integer 0-255 to XOR with
F467 C1      NOXOR: POP      BC      ; Restore AND mask
F468 CD3E10  WAITLP: CALL      INPSUB ; Get input
F46B AB      XOR        E          ; Flip selected bits
F46C A0      AND        B          ; Result non-zero?
F46D CA68F4  JP        Z,WAITLP    ; No - keep waiting
F470 C9      RET                ;

F471 CD84F4  SETIO:     CALL      GETINT ; Get integer 0-255
F474 323F10  LD        (INPRT),A    ; Set input port
F477 320710  LD        (OUTPRT),A   ; Set output port
F47A CD90E6  CALL      CHKSYN      ; Make sure ", " follows
F47D 2C      DEF      B          ;
F47E C384F4  JP        GETINT      ; Get integer 0-255 and return

F481 CD36E8  FNDNUM: CALL      GETCHR  ; Get next character
F484 CD41ED  GETINT: CALL      GETNUM ; Get a number from 0 to 255
F487 CD85E9  MAKINT: CALL      DEPRINT ; Make sure value 0 - 255
F48A 7A      LD        A,D        ; Get MSB of number
F48B B7      OR        A          ; Zero?
F48C C2A0E9  JP        NZ,FCERR    ; No - Error
F48F 2B      DEC        HL        ; DEC 'cos GETCHR INCs
F490 CD36E8  CALL      GETCHR      ; Get next character
F493 7B      LD        A,E        ; Get number to A
F494 C9      RET                ;

```

```

; << NO REFERENCE TO THIS SECTION OF CODE >>
; << Set up another program area (can be in ROM) >>

F495 2A5E10 LD HL,(BASTXT)
F498 22D610 LD (PROGND),HL
F49B 210080 LD HL,8000H
F49E 5E LD E,(HL)
F49F 23 INC HL
F4A0 56 LD D,(HL)
F4A1 23 INC HL
F4A2 23 INC HL
F4A3 225E10 LD HL,(BASTXT),HL
F4A6 EB EX DE,HL
F4A7 22AF10 LD (LSTRAM),HL
F4AA 225A10 LD (STRSPC),HL
F4AD 01F2E7 LD BC,RUNCNT
F4B0 C5 PUSH BC
F4B1 C3C5E4 JP RUNFST
F4B4 C356FD RUART: JP GUART

F4B7 CDBAF4 WUART2: CALL WUART
F4BA F5 WUART: PUSH AF
F4BB C5 PUSH BC
F4BC 4F LD C,A
F4BD C68FD CALL SUART
F4C0 C1 POP BC
F4C1 F1 POP AF
F4C2 C9 RET

CSAVE: LD B,1
F4C3 0601 ZTIMES CP
F4C5 FEAE Z,ARRSV1 JP
F4C7 CABBE8 CALL EVAL
F4CA CD5AED PUSH HL
F4CD E5 CALL GTFLNM
F4CE CD95F3 DE
F4D1 D5 PUSH CASFFW
F4D2 CDC8FC CALL DE
F4D5 D1 POP A,11010011B
F4D6 3ED3 LD WUART
F4D8 CDBAF4 WUART2
F4DB CDB7F4 CALL A,(DE)
F4DE 1A LD WUART
F4DF CDBAF4 CALL WUART
F4E2 00 NOP
F4E3 00 NOP
F4E4 00 NOP
F4E5 21D610 LD HL,PROGND
F4E8 220C0C LD (ARG1),HL
F4EB 2AD610 LD HL,(PROGND)
F4EE 220E0C LD (ARG2),HL
F4F1 CD73FE CALL SAVE
F4F4 CDB8FC CALL ARET
F4F7 E1 POP HL
F4F8 C9 RET

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F4F9 7E CLOAD: LD A,(HL)
F4FA FEAE ZTIMES CP
F4FC CAB9E8 Z,ARBLD1 JP
F4FF CDD1FF CALL SMOTOR
F502 D69E ZPRINT "?" ("PRINT" token) Verify?
F504 CA09F5 Z,FLGVER JP
F507 AF XOR A
F508 01 DEFB (LD BC,nn)
F509 2F FLGVER: CPL
F50A 23 INC HL
F50B F5 PUSH AF
F50C 2B DEC HL
F50D CD3E88 GETCHR
F510 3E00 LD A,0
F512 CA1CF5 Z,ANYNAM JP
F515 CD5AED EVAL
F518 CD95F3 CALL GTFLNM
F51B 1A LD A,(DE)
F51C 6F ANYNAM: LD L,A
F51D F1 POP AF
F51E F5 PUSH AF
F51F B7 OR A
F520 67 LD H,A
F521 22E410 (FPREG),HL
F524 CCBAB4 Z,CLRPTR CALL
F527 2AE410 HL,(FPREG)
F52A EB DE,HL
F52B 0603 CLOAD1: LD B,3
F52D CDBAF4 CLOAD2: CALL RUART
F530 D6D3 SUB 11010011B
F532 C22BF5 JP NZ,CLOAD1
F535 05 DEC B
F536 C22DF5 NZ,CLOAD2 JP
F539 CDBAF4 RUART
F53C CD74F5 CALL FTLPND
F53F 1C INC E
F540 1D DEC E
F541 CA48F5 Z,THSFIL JP
F544 BB CP E
F545 C22BF5 THSFIL: NOP
F548 00 NOP
F549 00 NOP
F54A 00 NOP
F54B F1 POP AF
F54C B7 OR A
F54D C25CF5 NZ,CLOADV JP
F550 CD88FE MONLD
F553 2AD610 HL,(PROGND)
F556 CD93E3 ENFMEM CALL
F559 C35FF5 CLOADV: CALL
F55C CDAAFE CLOADV: CALL
F55F 214BE3 HL,OKMSG
F562 CD10F2 CALL PRS
F565 CDB8FC ARET
F568 C37CE4 SETPTR JP

; Get byte after "CLOAD"
; "*" token? ("CLOAD*")
; Yes - Array load
; Start motor and get "?"
; "?" ("PRINT" token) Verify?
; Yes - Flag "verify"
; Flag "load"
; Skip "CPL" and "INC HL"
; Flag "verify"
; Skip over "?"
; Save verify flag
; DEC "cos GETCHR INCs
; Get next character
; Any file will do
; No name given - Any will do
; Evaluate expression
; Get file name
; Get first byte of name
; Save name to find
; Get verify flag
; And re-save
; Verify of load?

; Save nam of file to find
; Load - Clear pointers
; Get name of program to find
; Name to DE
; 3 Header bytes
; Get a byte from UART
; Header byte?
; Look for header
; Count header bytes
; More to find?
; Get name of file
; Display "file X found"
; Any file name given?

; No - This file will do
; Has file been found?
; No - Look for another

; Get verify flag
; Load or verify?
; Verify program
; Use monitor to load program
; Get end of program
; See if enough memory
; "Ok" and set up pointers
; Use monitor to verify program
; "Ok" message
; Output string
; Not a lot there!
; Set up line pointers

```

```

F56B 219DF5 OUTBAD: LD HL,BAD
F56E CD10F2 CALL PRS
F571 C3E1E3 JP ERRIN

F574 C5 FILFND: PUSH BC
F575 E5 PUSH HL
F576 D5 PUSH DE
F577 F5 PUSH AF
F578 218EF5 LD HL,FILE
F57B CD10F2 CALL PRS
F57E F1 POP AF
F57F F5 PUSH AF
F580 CDD9FC CALL COMMON
F583 2194F5 LD HL,FOUND
F586 CD10F2 CALL PRS
F589 F1 POP AF
F58A D1 POP DE
F58B E1 POP HL
F58C C1 POP BC
F58D C9 RET

"File",0
"Found",CR,LF,0
"Bad",0,0,0

F58E 4669C65 FILE: DEFB
F594 20466F75 FOUND: DEFB
F59D 42616400 BAD: DEFB

F5A3 CD8BE9 PEEK: CALL
F5A6 1A LD A,(DE)
F5A7 C301F1 JP PASSA

F5AA CD41ED POK: CALL
F5AD CD8BE9 CALL
F5B0 D5 PUSH DE
F5B1 CD90E6 CALL CHKSYN
F5B4 2C DEFB " "
F5B5 CD84F4 CALL GETINT
F5B8 D1 POP DE
F5B9 12 LD (DE),A
F5BA C9 RET

F5BB 2191FA ROUND: LD HL,HALF
F5BE CD62F8 LOADFP: CALL
F5C1 C32DF5 ADDPHL: JP FPADD

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F5C4 CD62F8 SUBPHL: CALL LOADFP
F5C7 21 DEFB (LD HL,nm)
F5C8 C1 PSUB: POP BC
F5C9 D1 POP DE
F5CA CD3CF8 SURCDE: CALL INVSGN
F5CD 78 FPADD: LD A,B
F5CE B7 OR A
F5CF C8 RET Z
F5D0 3AE710 LD A,(FPEXP)
F5D3 B7 OR A
F5D4 CA54F8 JP Z,FPBCDE
F5D7 90 SUB B
F5D8 D2E7F5 JP NC,NOSWAP
F5DB 2F CPL
F5DC 3C INC A
F5DD EB EX DE,HL
F5DE CD44F8 CALL STAKFP
F5E1 EB EX DE,HL
F5E2 CD54F8 CALL FPBCDE
F5E5 C1 POP BC
F5E6 D1 POP DE
F5E7 FE19 NOSWAP: CP 24+1
F5E9 D0 RET NC
F5EA F5 PUSH AF
F5EB CD79F8 CALL SIGNS
F5EE 67 LD H,A
F5EF F1 POP AF
F5F0 CD92F6 CALL SCALE
F5F3 B4 OR H
F5F4 21E410 LD HL,FPREG
F5F7 F20DF6 JP P,MINCDE
F5FA CD72F6 CALL PLUCDE
F5FD D253F6 JP NC,ROUNDUP
F600 23 INC HL
F601 34 INC (HL)
F602 CABCE3 JP Z,OVERR
F605 2E01 LD L,1
F607 CDABF6 CALL SRT1
F60A C353F6 JP RONDUP

F60D AF MINCDE: XOR A
F60E 90 SUB B
F60F 47 LD B,A
F610 7E LD A,(HL)
F611 9B SBC A,E
F612 5F LD E,A
F613 23 INC HL
F614 7E LD A,(HL)
F615 9A SBC A,D
F616 57 LD D,A
F617 23 INC HL
F618 7E LD A,(HL)
F619 99 SBC A,C
F61A 4F LD C,A
F61B DC7EF6 CONPOS: CALL C,COMPL

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F61E 68      BNORM: LD      L,B
F61F 63      LD      H,E
F620 AF      XOR      A
F621 47      BNRMLP: LD      B,A
F622 79      LD      A,C
F623 B7      OR      A
F624 C240F6  JP      NZ,PNORM
F627 4A      LD      C,D
F628 54      LD      D,H
F629 65      LD      H,L
F62A 6F      LD      L,A
F62B 78      LD      A,B
F62C D608    SUB      8
F62E FE00    CP      -24-8
F630 C221F6  JP      NZ,BNRMLP
F633 AF      RESZER: XOR      A
F634 32E710  SAVEXP: LD      (FPEXP),A
F637 C9      RET

F638 05      NORMAL: DEC      B
F639 29      ADD      HL,HL
F63A 7A      LD      A,D
F63B 17      RLA
F63C 57      LD      D,A
F63D 79      LD      A,C
F63E 8F      ADC      A,A
F63F 4F      LD      C,A
F640 F23BF6  PHORM: JP      P,NORMAL
F643 78      LD      A,B
F644 5C      LD      E,H
F645 45      LD      B,L
F646 B7      OR      A
F647 CA53F6  JP      Z,RONDUP
F64A 21E710  LD      HL,FPEXP
F64D 86      ADD      A,(HL)
F64E 77      LD      (HL),A
F64F D233F6  JP      NC,RESZER
F652 C8      RET
F653 78      RONDUP: LD      A,B
F654 21E710  LD      HL,FPEXP
F657 B7      OR      A
F658 FC65F6  CALL      M,FFROND
F65B 46      LD      B,(HL)
F65C 23      INC      HL
F65D 7E      LD      A,(HL)
F65E E680    AND      10000000B
F660 A9      XOR      C
F661 4F      LD      C,A
F662 C354F8  JP      FPRCDE

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F665 1C      FFROND: INC      E
F666 C0      RET      NZ
F667 14      INC      D
F668 C0      RET      NZ
F669 0C      INC      C
F66A C0      RET      NZ
F66B 0E80    LD      C,80H
F66D 34      INC      (HL)
F66E C0      RET      NZ
F66F C3BCE3  JP      OVERR

F672 7E      FLUCDE: LD      A,(HL)
F673 83      ADD      A,E
F674 5F      LD      E,A
F675 23      INC      HL
F676 7E      LD      A,(HL)
F677 8A      ADC      A,D
F678 57      LD      D,A
F679 23      INC      HL
F67A 7E      LD      A,(HL)
F67B 89      ADC      A,C
F67C 4F      LD      C,A
F67D C9      RET

F67E 21E810  COMPL: LD      HL,SGNRES
F681 7E      LD      A,(HL)
F682 2F      CPL
F683 77      LD      (HL),A
F684 AF      XOR      A
F685 6F      LD      L,A
F686 90      SUB      B
F687 47      LD      B,A
F688 7D      LD      A,L
F689 9B      SBC      A,E
F68A 5F      LD      E,A
F68B 7D      LD      A,L
F68C 9A      SBC      A,D
F68D 57      LD      D,A
F68E 7D      LD      A,L
F68F 99      SBC      A,C
F690 4F      LD      C,A
F691 C9      RET

F692 0600    SCALE: LD      B,0
F694 D608    SCALLP: SUB      8
F696 DA41F6  JP      C,SHRITE
F699 43      LD      B,E
F69A 5A      LD      E,D
F69B 51      LD      D,C
F69C 0E00    LD      C,0
F69E C394F6  JP      SCALLP

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Dis-assembly of NASCOM ROM BASIC Ver 4.7	PAGE 75	Dis-assembly of NASCOM ROM BASIC Ver 4.7	PAGE 76
F6A1 C609 SHRITE: ADD A,8+1 F6A3 6F LD L,A F6A4 AF XOR A F6A5 2D DEC L F6A6 C8 RET Z F6A7 79 LD A,C F6A8 1F RRA F6A9 4F LD L F6AA 7A LD A,D F6AB 1F RRA F6AC 57 LD D,A F6AD 7B LD A,E F6AE 1F RRA F6AF 5F LD E,A F6B0 78 LD A,B F6B1 1F RRA F6B2 47 LD B,A F6B3 C3A4F6 JP SHRLP F6B6 00000081 UNITY: DEFB 000H,000H,000H,081H ; 1.00000 F6BA 03 LOGTAB: DEFB 3 ; Table used by LOG F6BB AA561980 DEFB 0AAH,056H,019H,080H ; ?FC Error if <= zero F6BF F1227680 DEFB 0F1H,022H,076H,080H ; 0.59898 F6C3 45AA3882 DEFB 045H,0AAH,038H,082H ; 0.96147 ; 2.88539 F6C7 CD13F8 LOG: CALL TSTSGN F6CA B7 OR A F6CB EAA0E9 JP PE,FCERR F6CE 21E710 LD HL,FFEXP F6D1 7E LD A,(HL) F6D2 013580 LD BC,8035H F6D5 11F304 LD DE,04F3H F6D8 90 SUB B F6D9 F5 PUSH AF F6DA 70 LD (HL),B F6DB D5 PUSH DE F6DC C5 PUSH BC F6DD CDCDF5 CALL FPADD F6E0 C1 POP BC F6E1 D1 POP DE F6E2 04 INC B F6E3 CD69F7 CALL DVBCE F6E6 21B6F6 LD HL,UNITY F6E9 CDC4F5 CALL SUBPHL F6EC 21BAF6 LD HL,LOGTAB F6EF CD3EBF CALL SUMSER F6F2 018080 LD BC,8080H F6F5 110000 LD DE,0000H F6F8 CDCDF5 CALL FPADD F6FB F1 POP AF F6FC CD8EF9 RSCALE F6FF 013180 CALL BC,8031H F702 111872 LD DE,7218H F705 21 DEFB (LD HL,nn) 	; Adjust count ; Save bits to shift ; Flag for all done ; All shifting done? ; Yes - Return ; Get MSB ; Shift it right ; Re-save ; Get NMSB ; Shift right with last bit ; Re-save it ; Get LSB ; Shift right with last bit ; Re-save it ; Get underflow ; Shift right with last bit ; Re-save underflow ; More bits to do ; Test sign of value ; ?FC Error if <= zero ; Point to exponent ; Get exponent ; BCDE = SQR(1/2) ; Scale value to be < 1 ; Save scale factor ; Save new exponent ; Save SQR(1/2) ; Add SQR(1/2) to value ; Restore SQR(1/2) ; Make it SQR(2) ; Divide by SQR(2) ; Point to 1. ; Subtract FPREG from 1 ; Coefficient table ; Evaluate sum of series ; BCDE = -0.5 ; Subtract 0.5 from FPREG ; Restore scale factor ; Re-scale number ; BCDE = Ln(2) ; Skip "POP BC" and "POP DE" 	F706 C1 F707 D1 F708 CD13F8 F70B C8 F70C 2E00 F70E CDD1F7 F711 79 F712 32F610 F715 EB F716 22F710 F719 010000 F71C 50 F71D 58 F71E 211EF6 F721 E5 F722 212AF7 F725 E5E5 F727 21E410 F72A 7E F72B 23 F72C B7 F72D CA56F7 F730 E5 F731 2E08 F733 1F F734 67 F735 79 F736 D244F7 F739 E5 F73A 2AF710 F73D 19 F73E EB F73F E1 F740 3AF610 F743 89 F744 1F F745 4F F746 7A F747 1F F748 57 F749 7B F74A 1F F74B 5F F74C 78 F74D 1F F74E 47 F74F 2D F750 7C F751 C233F7 F754 E1 F755 C9 F756 43 F757 5A F758 51 F759 4F F75A C9 	; Get number from stack ; Test sign of FPREG ; Return zero if zero ; Flag add exponents ; Add exponents ; Get MSB of multiplier ; Save MSB of multiplier ; Save rest of multiplier ; Partial product (BCDE) = zero ; Address of normalise ; Save for return ; Address of 8 bit multiply ; Save for NMSB,MSB ; Point to number ; Get LSB of number ; Point to NMSB ; Test LSB ; Zero - shift to next byte ; Save address of number ; 8 bits to multiply by ; Shift LSB right ; Save LSB ; Get MSB ; Bit was zero - Don't add ; Save LSB and count ; Get LSB and NMSB ; Add NMSB and LSB ; Leave sum in DE ; Restore MSB and count ; Get MSB of multiplier ; Add MSB ; Shift MSB right ; Re-save MSB ; Get NMSB ; Shift NMSB right ; Re-save NMSB ; Get LSB ; Shift LSB right ; Re-save LSB ; Get VLSB ; Shift VLSB right ; Re-save VLSB ; Count bits multiplied ; Get LSB of multiplier ; More - Do it ; Restore address of number ; Shift partial product left

AUNT AGATHA'S AGONY COLUMN**By David Parkinson****GM809, GM829 compatibility/upgrading**

This is a question that has cropped up several times recently. The answer is that the Gemini GM829 FDC/SASI board can be regarded as a GM809HL. With the 'HL' level of trim you gain software controlled 5.25"/8" switching together with a SASI interface. Other than that the products are identical (ports, software interface etc). For some one currently running a system with GM809, upgrading is a matter of a) getting a GM829; b) Checking the straps; c) Plugging it in. That's all there is to it.

The software controllable 5.25"/8" switching of GM829 is quite useful - even if you don't have 8" drives connected to your system. This is because the Western Digital Floppy disk controller used on GM809/GM829 is limited in the maximum rate it can step the head between tracks on the attached drives. When set for 5.25" drives it can only achieve a 6ms stepping rate, but the modern Japanese drives (e.g. TEAC FD55Es & Fs) can be stepped at 3ms/step. Running them at the slower rate results in reduced performance and a 'graunching' noise from the drive (non destructive!). However, with GM829, whenever a SEEK is required, the 5.25"/8" control can be flipped to 8". The main effect of this is to double the clock frequency to the Controller chip, which results in all stepping times being halved. Thus drives can now be stepped as fast as 3ms/step, leading to increased performance and much quieter stepping. (Once the seek is complete, the control bit is obviously flipped back to the 5.25" setting before doing the read/write operation.)

BASIC mathematics

A letter from Phil Dunglinson on the topic of a BASIC program that doesn't work provides me with my next topic. The listing is shown below:

```
10 FOR N3=1 TO 9
20 FOR N2=0 TO 9
30 FOR N1=0 TO 9
40 A= N1^3 + N2^3 + N3^3
50 B= N1 + N2*10 + N3*100
60 IF A=B THEN 80
70 GOTO 90
80 PRINT B
90 NEXT N1
100 NEXT N2
110 NEXT N3
120 END
```

It should print out the results of 153,370,371,407 but doesn't. Can anyone see why not?

The answer is simple, and reminds me of that old adage about not blindly accepting the answer that comes out of a computer. Just because your computer tells you that $2 <> 2$ or $3 = 2$ does not necessarily mean that it is true. Remember that your computer is an idiot and tries to do exactly what you tell it. It can do mundane operations very quickly, but it does have limitations and this example highlights two of them.

Point 1: Derived Arithmetic Functions.

To us normal Human Beings $N1^3$ ($N1$ raised to the power 3) in line 40 of the above program means $N1*N1*N1$. However to Nascom BASIC it means: $EXP(3*LOG(N1))$, where both the $LOG()$ and the $EXP()$ are calculated by evaluating polynomial approximations of the form:

$$C0 + C1*X + C2*X^2 + C3*X^3 + \dots + CN*X^N$$

X is the value passed to the approximation routine, and $C0, C1, \dots, CN$ are constants whose value depend upon the approximation required (EXP , LOG , SQR etc). Usually X has to be scaled to lie within a certain range for the approximation to be valid. The accuracy of the approximation obviously depends upon the degree of the polynomial, and for some functions a polynomial of low degree (of only 3 say) can be suprisingly accurate. From a computational speed

point of view, the shorter the polynomial, the faster it can be evaluated. For those who wish to pursue matters further various books can be found covering the topic. One fairly comprehensive book I have encountered is [1].

So point one is that derived functions such as " $\wedge$ ", LOG, EXP, etc are written for the general case and have approximate results. (They may be accurate, but not necessarily exact.) They do not recognise specific cases (such as the exponent in a " $\wedge$ " expression being a small integer) and do not adjust their algorithms accordingly. This means that the instruction in line 60 (IF A=B THEN...) is almost certainly going to be false. If A=2 and B=1.999999999..... then they are not equal in the eyes of the computer although an engineer would happily accept them as such. (Mind you a Scientist may not, but that leads on to the old joke...) Therefore line 60 has to be rephrased as - IF A EQUALS B FOR ALL PRACTICAL PURPOSES THEN This can best be done by coding it as IF ABS(A-B)<1E-4 THEN.... Here we have said if the two values are within 1/10,000 of each other then take them as equal. After making this change the program above will run successfully.

Alternatively the program can be recoded to make the calculation more accurate. By recoding line 40 as ... $N1*N1*N1 + N2*N2$ we replace the approximation by an accurate calculation. (Accurate in this case as we are dealing with reasonably sized INTEGERS. In other circumstances - e.g. N1 etc being REAL numbers like 2.345 and 7.916 - there would be rounding errors and possible dynamic range problems affecting the accuracy of the result.) This change also has the side effect of speeding up the program as the two multiplications are faster than the $\wedge$ function. In making this change to line 40, line 60 can be left as IF A=B... (but remember the caveat above).

Point 2: Binary Arithmetic

While we are on the topic of computer accuracy I'll just mention one other point. Computers that use binary arithmetic cannot hold most decimal fractions accurately. (e.g. 2.67 might be held as 'a number very close to 2.67'.) This is why any serious financial program always uses BCD (Binary Coded Decimal) arithmetic - where 2.67 IS 2.67 - rather than pure binary arithmetic. That way the books generally balance exactly rather than approximately as the arithmetic exactly matches the human 'pencil & paper' mode. Details of BCD algorithms can be found in [2].

Software Testing

The example above highlights another important point that we are all frequently guilty of, and that is inadequate testing of programs. This program is not perhaps the best example as it does not process any external data in order to produce its result. The important point though is that Phil Duglinson knew what the program should do (in its present form) and when the correct results didn't emerge he knew there was a bug to find. Software testing is an art. (Think of the unlimited character combinations possible in the source input file for an assembler or compiler.) There are various books on the topic that you can read if you are interested [3][4]. I don't intend to cover software testing here, but one thing to remember is that any program that processes data, as well as producing correct output from correct input, must not accept incorrect input without complaining, or crash when presented with the unexpected.

Just to give a few examples of what I've encountered: First a minor bug illustrating what can happen to a program presented with the unexpected. With C/80 version 2.0 the compiler carried on compiling a source file past the end-of-file marker if the file ended in a TAB character rather than the usual CR/LF pair. (Easy enough to end up with a TAB if you use an on-screen editor.)

Next a minor bug but slightly more serious. Old habits die hard, and in some C/80 source files I entered a few hexadecimal constants with a trailing 'H' (e.g. 0xAH rather than the correct 0xA). The compiler accepted these with out comment and interpreted the 'H' as part of the Hex number so I ended up with an incorrectly evaluated constant. Finally my latest encounter has been with an unforgiveable bug from Microsoft in an 8086 cross assembler. Being relatively inexperienced in 8086 assembly language programming my initial programs contained some glaring errors like loading a segment register with an immediate 16-bit value. (Although you can load normal registers with immediate data, the segment registers can only be loaded with data from another register, or from memory.) The Microsoft assembler accepted my illegal instructions without complaint, and proceeded to generate an opcode for a completely different instruction. It was only by manually dis-assembling the opcodes on the assembly listing that I found the error. (Luckily, as I was just starting to familiarise myself with the 8086, the program was only some 25 lines long and it didn't long to find the error.)

One final comment on this topic - if you do find a bug REPORT IT, not by complaining to your friends, but to the Author. Document the bug thoroughly, for example by writing a four or five line program to illustrate it, and send the print-outs in with your comments. DON'T ASSUME SOMEBODY ELSE MUST HAVE ALREADY REPORTED IT. You might find that your report vanishes into a black hole, or, as I did with the Software Toolworks, get a polite note back and find the bugs corrected in the next release of the software (C/80 version 3.0).

Do-it-yourself Electrocution. (c.f. Richard Beal in the last issue!)

Next a request from Mr E.Jones for an article covering the general principles of converting a domestic TV set to a monitor, and interfacing a NASCOM to it. These sort of articles tend to be few and far between, and I assume that the reason for this is that no magazine editor wishes to be sued by the relatives of a late hobbyist who attempted to follow the article. **VOLTAGES INSIDE A TV SET OR MONITOR ARE LETHAL.** If you do not know what you are doing **LEAVE THE BACK ON THE SET.** If you think you know what you are doing I still advise you to leave the back on the set. With the proliferation of home computers and video recorders that has occurred in recent years it should be possible to pick up a cheap black-and-white monitor, or to buy a TV set that already has a video input socket on the rear. For those who want to progress further I offer the following suggestions/observations.

- (1) Before doing anything buy a copy of the service sheet or service manual for the set in question.
- (2) Look at the power supply section of the diagram. Many sets directly rectify the mains input and then use the filtered DC supply either directly, (old Valved sets), or via a switch-mode step-down power supply (newer integrated sets). Certainly in the first case, and possibly in the second, you will find that one side of the TV chassis is connected directly to the NEUTRAL of the mains supply, (or directly to the LINE side if the plug has been wired incorrectly). In this case **DO NOT PROCEED FURTHER** unless you can isolate the TV supply from the mains, or provide a suitable barrier at the video interface into the TV. (A small black-and-white portable offering mains/battery operation will almost certainly have a mains transformer providing the required isolation.)
- (3) The signal present at the VIDEO pin on the Nascom 2 circuit board is a conventional composite video signal containing a mixture of Video and the horizontal and vertical sync pulses. This is similar to the signal present at the output of the detector following the IF stages in the TV. I haven't looked

at any TV circuit diagrams recently, but with transistorised sets this point can be found relatively easily. With a modern set full of ICs you may find that the point you're after lies within an IC. You need to inject the composite video signal (of the appropriate amplitude and polarity) at a point just before the video and sync signals are separated.

(4) ALWAYS SWITCH OFF AND ALLOW TIME FOR HIGH VOLTAGES TO DISCHARGE BEFORE MAKING ANY CHANGE TO YOUR CIRCUITS.

(5) If you must make any internal adjustments on a live set always use only one hand, keeping the other well out of the way (like in your pocket). DON'T GROPE ROUND THE BACK OF THE SET. If you need to see the screen while you make the adjustment sit behind the set and use a mirror. ALWAYS KEEP AN EYE ON THE HAND MAKING THE ADJUSTMENTS. As well as high voltages there are usually HOT resistors in the set. If you touch one of those inadvertently who knows what your reflex action may make you come in contact with, and you may end up with far more than a burnt hand.

CP/M Users Group

Some time ago I mentioned the CP/M Users Group. The CP/M Users Group (UK) publishes a magazine (quarterly?) that varies in content. It normally contains news, reviews and comment. In the last issue (March 1984) over half the magazine was a listing of the new UK and SIG/M disks that have recently been added to the library. Currently a years subscription costs \$7.50. The CP/M Users Group can be found at 72 Mill Road, Hawley, Dartford, Kent DA2 7RZ.

Using a Nascom as a counter-timer.

Next on the pile is a rather confused letter from someone in Southampton who had better remain nameless. (Anyway we reckon the name was a pseudonym.) He is wanting to use the interrupt line on a Nascom to measure the duration and frequency of a pulse stream. For some obscure reason (not stated) he wants to use interrupt mode 1. This effectively executes a 'RST 38' in response to an interrupt, vectoring to address 38H. However Nas-Sys has its "RDEL" routine at that address which is not much use in measuring anything! As a result he has got tied up in knots trying to get a RAM based version of Nas-Sys going. He is trying to do this so that he can patch the RST 38 location to jump to his own routine. I'll ignore the mire he has got into with his attempt to get Nas-Sys into RAM and point out the large errors he has made and a possible approach to the problem.

1) The interrupt problem.

Use Mode 2. There is nothing mysterious about it, it just needs a little thought as there are two levels of indirection to go through before you arrive at your service routine which can be anywhere in memory. If you must use mode 1 why not use NMI instead of INT? In response to an NMI, Nas-Sys 3 vectors via a JP stored in RAM at 0C7D. (i.e. At address 66 - the NMI execution address - Nas-Sys has a JP 0C7D. It initialises 0C7D to a JP <register display routine>.) Thus anyone wanting to use the NMI for another purpose can change the JP stored in the workspace area so that control passes to their routine, rather than back into Nas-Sys. (If necessary you connect your interrupt signal via a 2-input OR gate (e.g. 74LS32) to the NMI input. By connecting the other input of the OR gate to a spare bit on the PIO you can have a 'maskable' NMI).

2) Why there is no point in doing it anyway.

To go back to his original requirement. "To input a stream of variable length pulses direct into the Z80 pin 16 /INT, and thereafter analyse their lengths & groupings by program". He has started off by making a fundamental

error. The INT input to the Z80 is level sensitive. i.e. While the /INT signal is low an interrupt will occur immediately interrupts are enabled. I cannot conceive of how the /INT pin could be reasonably used to measure the duration of pulses. (The only thing that comes to mind is a service routine that starts EI, NOP, followed by code that counts the number of return addresses that have been pushed onto the stack by the successive interrupts - hardly a practical proposition.) The situation with the NMI input is virtually the same. The only difference is that it is an edge triggered input. The NMI is generated on the high-to-low transition of the signal on this pin and the low-to-high transition has no effect. Therefore a program could collect statistics of the frequency of occurrence of the pulses, but could not determine anything about their duration. Neither pin is suitable for the measurement of pulse widths.

3) Try the PIO.

The obvious candidate for this task is the PIO. It can be set into mode 3 (control mode) and the levels present on one or more of the external inputs can be monitored by the control software. But before anything can be done we need to know something about the characteristics of the pulses that are to be monitored. Obviously if they are very short (of the order of 1 μ S) and very frequent there is no way that the Z80 can be used for this purpose. Pulses of milliseconds or seconds duration can be measured relatively easily, but a few calculations should be done to determine the relative accuracy obtainable, and whether it is acceptable.

A simple timing loop could be:

```
ld    hl,0      ; Clear counter
loop: inc hl     ; Bump counter
      in  a,(pio) ; Read port
      rlca      ; Put bit into carry
      jp  c,loop ; Loop if still ON
      ....      ; Check count & save data
```

With this there is obviously an uncertainty equal to the software loop time in determining when the pulse goes OFF. (It could go OFF just before or just after the IN instruction is executed. In the latter case another complete loop is executed although the time difference between the two cases could only be nanoseconds.) Similarly there can be a similar uncertainty in recognising the ON instant. The basic unit measurement here is the 'LOOP' (above) and we can measure to an accuracy of ± 2 LOOPS.

The above is a very simplified view of matters as there is also the problem of measuring the intervals between pulses. (Note also the above loop assumes that a 16-bit counter is adequate as the loop counter. For long pulses this is unlikely to be so, and the loop will have to be expanded to include an overflow check of the counter. However, as the pulse is longer, we can accept a greater absolute error as the relative error will still probably be small.)

The only thing to do is to sit down and write some of the software and start counting clock cycles. You may find it advantageous to add dummy instructions into some conditional paths so that all routes from point A in the software to point B take about the same amount of time. That way a software timer-counter can be updated with a fixed number to compensate for the execution of a particular service routine. (e.g. The routine that takes the last pulse measurement and updates a histogram table based upon the pulse width in 'loop counts'.)

Life may be easier if you use an I/O board which includes a CTC (Counter Timer Circuit).

NASCOM(?) 1.5(?)

Finally a letter from Steve Waites which I reproduce below:

"Back in the dark annals of history I had a perfectly good and working Nascom 1 + RAM A card. Simple but effective. Then I left to work in America so the poor old computer got left behind. Two years later I returned, and found that the old Nascom had pined itself away to an early demise. Checking the boards I found several of the 2102 RAMs and a few TTL chips had gone to the great semiconductor heaven. In desperation, and because I only had two weeks in England, I grabbed all the relevant software and made the decision to rebuild and upgrade. Several months of hard effort later I now welcome the Nascom 1.5. Why 1.5? Well its no longer a Nascom 1, nor is it quite a Nascom 2. My additions are an enhanced RS232 port, 18K of CMOS RAM on the main board plus 4118 RAMs for the video and user areas. Oh yes it also has a sound chip, a battery backed RAM area, graphics, and the memory is fully decoded in 2K blocks by a 74LS154.

"So far so good - everything works wonderfully - now to my question. Is there anyway that I can get my ZEAP (level 2.1 in ROM) to output continuously to the UART port as I usually use my computer with a slave CRT connected to the RS232 interface rather than the internal video. Also, but not quite so important, I have the Bits & PCs programmers toolkit. It won't accept commands from the remote CRT keyboard. Both Nas-Sys and BASIC have no problems in this respect, so what is the difference?"

Perhaps some of you ZEAP hackers can write in with a solution for Steve. I believe that ZEAP writes directly into the bottom line of the NASCOM display. This is done for speed, as outputting a character at a time via Nas-Sys would have an impact on the already slow assembly speed of ZEAP. How much of ZEAP's output goes this way I have no idea. As for the Bits & PCs toolkit, this uses its own input and output tables, and also utilises UIN and UOUT. I assume that a small error must exist in this somewhere which prevents it scanning the serial input port. Perhaps some enterprising person who is using the toolkit can send in a suitable patch that can be published in a subsequent issue of the NEWS?

Ta Ta.

References:

1. HART J.F, CHENEY E.W. et al, "Computer Approximations", Pub: John Wiley 1978
2. SCHMID H., "Decimal Computation", Pub: John Wiley 1974
3. MYERS G.J., "The Art of Software Testing", Pub: John Wiley 1979.
4. BRUCE R.C., "Software debugging for Microcomputers", Reston Publ. Co. 1980

Arfon Speech Board - #50. 16K RAM A board - #15. 32K RAM B board - #40. Eascomp PSG board (minus AY-3-8910 chip) - #10. Machine Code Programming for the Nascom Book - #2. Phone Kevin on 0224-36160 (evenings).

IBM Selectric KBD printer, ex. 2741, with hardware/software interface for Nascom & Nas-Sys. Uses 8 bits of PIO. Previously IBM maintained, in excellent working order. Sensible offers please to Ian on Ipswich (0473) 831353.

Polydos File Update

By M. J. R. Gibbs

This is a program for directly updating files on disk for a Gemini GM809/GM815 system with Polydos 2.0.

This allows the user to update copies of a file stored on several disks without having to enter all the normal commands which can cause problems especially if you are like me and sometimes forget to use the 'NEW' command (I have spent many hours trying to sort out the chaos that this causes). The required program/data is loaded into RAM between #1000 and #C000 either by using the Polydos 'Read' command or by Assembling the program directly into RAM.

This program is loaded into RAM at #0C80 and executed at #0C80, the user is asked for the file name, file extension and the RAM location for the new code. The Program asks you to insert the disks and press the 'Enter' key, the disk directory is read and the filename checked and, should it exist, the file is replaced with the new version. The old file is completely overwritten and this means that the number of sectors used by the old file are replaced by the program stored in RAM. The user should be careful in ensuring that the new version is not going to require more sectors on disk than the original otherwise the whole of the new program will not be saved. I normally save programs that are common to several disks with a few sectors more than required to allow for expansion, this does not normally cause a problem as I have found that the maximum number of fifty files means that the disk is not often full before the directory is used up. This program can update a disk with a full directory because the directory is left unaltered.

Should the program not exist on the disk inserted a suitable message is printed out telling the user and the disk is left unaltered.

The Program informs the user of the start disk sector and the number of sectors updated and the RAM location used for the update. Should there be any disk errors then a message is printed out and the program repeats the initial menu asking the user to insert a new disk. The one thing that I have found is that it is easy to forget to remove any write protect tabs and this does not cause a problem as you simply take the disk out remove the tab and reinsert the disk (do not forget to replace the tab afterwards).

A word of warning when testing this program especially when entering it from the dump listing, which is always prone to errors, you are strongly advised to test it on a copy of an existing disk because it can overwrite irreplaceable portions of disk if anything goes wrong.

I suggest that you proceed as follows:-

- 1) Enter the program and save it on disk using Polydos. Use the 'Read' routine to load a file from the disk to say #1000 in RAM.
- 2) Examine and make a note of the number of sectors that this file uses.
- 3) Use SZAP to examine the next file on the disk and make a note of the first few bytes.
- 4) Change the first few bytes of the file loaded into RAM with the NAS-SYS 'M' (modify command).
- 5) Run this program to update the disk and check the user information for the correct sector location, number of sectors and RAM location.
- 6) Use SZAP to verify that the original file has been updated correctly. Also that the file following is not corrupted.

Below is a full Assembly listing, a sorted symbol table and a dump of the program using a modified version of the disk dump published in Vol.1 iss. 2 of 80-BUS NEWS (the numbers on the left hand side are the RAM locations).

Z2 ASSEMBLY LISTING PAGE 1

```

OF35 *****
OF35 * DISK UPDATE ROUTINE *
OF35 *
OF35 *
OF35 *
OF35 * M.J.R GIBBS 29-12-82 *
OF35 *****
0018 NASSYS EQU #18
0028 PRS EQU #28
0030 ROUT EQU #30
0030
007B BLINK EQU #7B
0068 B2HEX EQU #68
0065 CRT EQU #65
0063 INLIN EQU #63
0064 NUM EQU #64
005B RETNAS EQU #5B
0066 TBCD3 EQU #66
005D TDEL EQU #5D
005D
0080 ZDSIZE EQU #80
0081 ZDRD EQU #81
0082 ZDWR EQU #82
0083 ZRDIR EQU #83
0084 ZWDIR EQU #84
0085 ZCFS EQU #85
0087 ZENTER EQU #87
0088 ZCOV EQU #88
0089 ZCOVR EQU #89
008A ZCKER EQU #8A
008B ZCBRK EQU #8B
008C ZCFMA EQU #8C
0086 ZLOOK EQU #86
0063 ZINLIN EQU #63
0063
C055 SIFCB EQU #C055
C069 S2FCB EQU #C069
C001 DRV EQU #C001
C414 NXTSEC EQU #C414
C414
C055 ORG SIFCB
C055 LOAD 0
C055
C055 FNAM DS 8
C05D FEXT DS 2
C05F FSFL DS 1
C060 FUFL DS 1
C061 FSFC DS 2
C063 FNSC DS 2
C065 FLDA DS 2
C067 FEFA DS 2
C067
080A LINE1 EQU #080A
OC29 CURSOR EQU #OC29

```

Z2 ASSEMBLY LISTING PAGE 2

```

OC21 NUMV EQU #OC21
000C CLEAR EQU #OC
000D CR EQU #OD
001B ESC EQU #1B
001B
0C80 ORG #OC80
0C80 LOAD #8000
0C80
0C80
0C80
0C80 CD230E UPDATE CALL HEAD
0C83 EF RST PRS
0C84 4469736B
0C88 20557064
0C8C 61746520
0C90 46696C65
0C94 204E616D
0C98 65202E2E
0C9C 2E3A2D20
0CA0 00
0CA1 DF
0CA2 63
0CA3 211C00
0CA6 19
0CA7 1155C0
0CAA 010800
0CAD EDB0
0CAF EF
0CB0 OD
0CB1 46696C65
0CB5 20457874
0CB9 656E7369
0CBD 6F6E202E
0CC1 2E2E2E2E
0CC5 2E2E2E2E
0CC9 2E3A2D20
0CCD 00
0CCE DF
0CCF 63
0CD0 211C00
0CD3 19
0CD4 115DC0
0CD7 010200
0CDA EDB0
0CDC EF
0CDD OD
0CDE 52414D20
0CE2 53746172
0CE6 74204164
0CEA 64726573
0CEE 73202E2E
0CF2 2E2E2E2E
0CF6 2E3A2D20

```

;

; --- DISK UPDATE ---

; WRITE HEADINGS

'Disk Update File Name',0

DB RST

NASSYS

DB ZINLIN

LD HL,28

ADD HL,DE

LD DE, FNAM

LD BC,8

LDIR

RST PRS

DB CR

;GET REPLY

;HL = A (START OF REPLY)

;DE = A (SIFCB)

;LOAD NAME

'File Extension',0

DB RST

NASSYS

DB INLIN

LD HL,28

ADD HL,DE

LD DE, FEXT

LD BC,2

LDIR

RST PRS

DB CR

;HL = A (EXT)

;DE = A (FEXT)

;LOAD FEXT

```

OCFA 00      DB      'RAM Start Address .....: ',0
OCFB 2A290C  LD      HL,(CURSOR) ;HL = CURSOR LOCN
OCFE 22290C  ERR10   LD      (CURSOR),HL ;RESET CURSOR
OD01 E5      PUSH    HL ;KEEP CURSOR LOCN
OD02 DF      RST     NASSYS
OD03 63      DB      INLIN
OD04 D1      POP     DE
OD05 E5      PUSH    HL
OD06 DF      RST     NASSYS
OD07 64      DB      NUM
OD08 E1      POP     HL
OD09 38F3    JR      C,ERR10
OD0B 2A210C  LD      HL,(NUMV)
OD0E 22350F  LD      (RAMPOS),HL ;KEEP REPLY
OD0E         ;MAIN LOOP FOR LOADING FILES
OD0E         ;
OD11 CD680E  LOOP    HEAD2
OD14 DF      RST     NASSYS
OD15 7B      DB      BLINK
OD16 FE1B    CP      ESC
OD18 3A1C0E  JP      Z,DSKEND
OD1B 3EFF    LD      A,#FF
OD1D 3201C0  LD      C,0
OD20 0E00    LD      C,0
OD22 DF      RST     NASSYS
OD23 83      DB      ZRDIR
OD24 20EB    JR      NZ,LOOP
OD26 2153C0  LD      HL,SIFCB
OD29 0630    LD      B,#30
OD2B DF      RST     NASSYS
OD2C 86      DB      ZLOOK
OD2D 2829    JR      Z,LOADIT
OD2F EF      RST     PRS
OD30 0D0D    DB      CR,CR
OD32 20202020
OD36 2020203C
OD3A 3D3D3D3D
OD3E 2046494C
OD42 45204E4F
OD46 5420464F
OD4A 554E4420
OD4E 3D3D3D3D
OD52 3E00    DB      NASSYS
OD54 DF      RST     TDEL
OD55 5D      DB      LOOP
OD56 18B9    JR      LOOP
OD56         ;
OD56         ;
OD56         ;ALL OK LOAD DISK
OD56         ;SHOW ATTRIBUTES FOR CHECK
OD56         ;
OD58 CD230E  LOADIT  CALL HEAD
OD5B 2A61C0  LD      HL,(FSEC)

```

```

OD5E EF      RST     PRS
OD5F 20202020
OD63 20202044
OD67 69736820
OD6B 53656374
OD6F 6F72202E
OD73 2E2E2E2E
OD77 2E2E2E3A
OD7B 3D2000  DB      Disk Sector .....: ',0
OD7E DF      RST     NASSYS
OD7F 66      DB      TBCD3
OD80 2A350F  LD      HL,(RAMPOS) ;HL = RAM POSN-
OD83 EF      RST     PRS
OD84 0D0D    DB      CR,CR
OD86 20202020
OD8A 20202046
OD8E 726F6D20
OD92 52616D20
OD96 2E2E2E2E
OD9A 2E2E2E2E
OD9E 2E2E2E3A
ODA2 3D2000  DB      From RAM .....: ',0
ODA5 DF      RST     NASSYS
ODA6 66      DB      TBCD3
ODA7 3A63C0  LD      A,(FNOSC) ;A = NUMBER OF SECTORS
ODA8 47      LD      B,A ;B = NUMBER OF SECTORS
ODAB EF      RST     PRS
ODAC 0D0D    DB      CR,CR
ODAE 20202020
ODB2 2020204E
ODB6 756D6265
ODBA 72206F66
ODBE 20536563
ODC2 746F7273
ODC6 202E2E3A
ODCA 3D202020
ODCE 00      DB      Number of Sectors ...: ',0
ODCF 78      LD      A,B ;A = NUMBER OF SECTORS
ODD0 DF      RST     NASSYS
ODD1 68      DB      B2HEX
ODD2 C5      PUSH    EC ;WAIT TO SHOW
ODD3 DF      RST     NASSYS
ODD4 5D      DB      TDEL
ODD5 C1      POP     BC ;RECOVER BC
ODD6 0E00    LD      C,0 ;C = DISK DRIVE
ODD8 ED3B61C0 LD      DE,(FSEC) ;DE = A(SECTOR)
ODDC 2A350F  LD      HL,(RAMPOS) ;HL = A(RAM)
ODDF DF      RST     NASSYS
ODE0 82      DB      ZDWR ;WRITE IT OUT
ODE1 2003    JR      NZ,ERRORD
ODE3 C3110D  JP      LOOP ;NEXT
ODE6 F5      ERRORD  PUSH AF ;KEEP ERROR NUMBER
ODE7 EF      RST     PRS

```

```

ODE8 0D0D4572
ODEC 726F7220
ODF0 3D3D3D3E
ODF4 2000
ODF6 F1
ODF7 DF
ODF8 88
ODF9 456D7367
ODFD EF
ODFE 0D0D
OE00 20202020
OE04 20202050
OE08 72657373
OE0C 2022456E
OE10 74657222
OE14 202000
OE17 DF
OE18 78
OE19 C3110D
OE1C 3EFF
OE21 3201C0
OE21 DF
OE22 5B
OE23 EF
OE24 0C
OE25 20202020
OE29 20202020
OE2D 20202020
OE31 20444953
OE35 4B205550
OE39 44415445
OE3D 20535449
OE41 4C495459
OE45 0D
OE46 20202020
OE4A 20202020
OE4E 20202020
OE52 202D2D2D
OE56 2D2D2D2D
OE5A 2D2D2D2D
OE5E 2D2D2D2D
OE62 2D2D2D2D
OE66 0D0D0D00
OE6A C9
OE6B CD230E
OE6E EF
OE6F 20202020
OE73 20202046
OE77 696C6520
OE7B 4E616D65
OE7F 202E2E2E
OE83 2E2E2E2E
OE87 2E2E2E3A

DB CR,CR,"Error ==> ",00
AF
RST
DB ZGOV
DB "Emsg"
RST PRS
DB CR,CR

;LOAD ERROR OVERLAY
;PRINT MESSAGE

Press "Enter" ',0

DB NASSYS
RST BLINK
DB LOOP
DSKEND LD A,#FF
OE1C 3EFF
LD (ODRV),A
RST NASSYS
DB RETNAS
RST PRS
DB CLEAR

HEAD
OE23 EF
OE24 0C
OE25 20202020
OE29 20202020
OE2D 20202020
OE31 20444953
OE35 4B205550
OE39 44415445
OE3D 20535449
OE41 4C495459
OE45 0D
OE46 20202020
OE4A 20202020
OE4E 20202020
OE52 202D2D2D
OE56 2D2D2D2D
OE5A 2D2D2D2D
OE5E 2D2D2D2D
OE62 2D2D2D2D
OE66 0D0D0D00
OE6A C9
OE6B CD230E
OE6E EF
OE6F 20202020
OE73 20202046
OE77 696C6520
OE7B 4E616D65
OE7F 202E2E2E
OE83 2E2E2E2E
OE87 2E2E2E3A

DISK UPDATE UTILITY'

DB CR
DB

DB CR,CR,CR,0
RET CALL HEAD
OE6E EF
OE6F 20202020
OE73 20202046
OE77 696C6520
OE7B 4E616D65
OE7F 202E2E2E
OE83 2E2E2E2E
OE87 2E2E2E3A

;----- SUBROUTINE HEAD2 -----
RST PRS

```

```

OE8B 2D2000
OE8E 2155C0
OE91 0608
OE93 7E
OE94 F7
OE95 23
OE96 10FB
OE98 EF
OE99 2E00
OE9B 0602
OE9D 7E
OE9E F7
OE9F 23
OEAO 10FB
OEAA EF
OEAB 0D0D
OEAC 2A350F
OEAD 6F616465
OEAE 2020204C
OEAF 2020204C
OEB1 64204672
OEB5 6F6D2052
OEB9 414D202E
OEBD 2E2E2E3A
OEC1 2D2000
OEC4 2A350F
OEC7 DF
OEC8 66
OEC9 EF
OECA 0D0D
OECB 20202020
OECF 20202049
OED0 20202049
OED4 6E736572
OED8 74204469
OEDC 73682069
OEE0 6E746F20
OEE4 44726976
OEE8 6520302E
OEEC 0D
OEEF 20202020
OEF1 20202050
OEF5 72657373
OEF9 2022456E
OEFD 74657222
OE01 20776865
OE05 6E207265
OE09 6164792E
OE0D 0D
OE0E 20202020
OE12 20202050
OE16 72657373
OE1A 20224573
OE1E 63222020
OE22 20746F20

File Name .....: ',0
HL,SIFCB
LD B,8
LD A,(HL)
RST ROUT
INC HL
DINZ HEAD10
RST PRS
DB ',',0
LD B,2
HEAD20 LD A,(HL)
RST ROUT
INC HL
DINZ HEAD20
RST PRS
DB CR,CR

Loaded From RAM .....: ',0
HL,(RAMPOS)
RST NASSYS
DB TBCD3
RST PRS
DB CR,CR

Insert Disk into Drive 0.'
DB CR
DB CR

Press "Enter" when ready.'
DB CR
DB CR

```

```

OF26 5465726D
OF2A 696E6174
OF2E 652E3A2D
OF32 20          DB      '      Press "Esc"      to Terminate.-- "
OF33 00          DB      0
OF34 C9          RET
OF35 0100        RAMPOS DW 1

```

```

E2HEX 0068 BLINK 007B CLEAR 000C CR 000D
CRT 0065 CURSOR 0C29 DDVV 0001 DSKEND 0E1C
ERR10 0CFE ERROR 0DE6 ESC 001B FEXA C067
FEXT C05D FLDA C065 FNAM C055 FNSC C063
FSEC C061 FSFL C05F FUFL C060 HEAD 0E23
HEAD10 0E93 HEAD2 0E6B HEAD20 0E9D INLIN 0063
LINE1 080A LOADIT 0D58 LOOP 0D11 NASSTYS 0018
NUM 0064 NUMV 0C21 NITSEC C414 PRS 0028
RAMPOS 0F35 RETNAS 005B ROUT 0030 SIFCB C055
S2FCB C069 TRCD3 0066 TDEL 003D UPDATE 0C80
ZCBRK 008B ZCFMA 008C ZCFS 0085 ZCKER 008A
ZCOV 0088 ZCOVR 0089 ZDRD 0081 ZDSIZE 0080
ZDWR 0082 ZENTER 0087 ZINLIN 0063 ZLOOK 0086
ZDRIR 0083 ZDIR 0084

```

```

0C80 CD 23 0E EF 44 69 73 6B 20 55 70 64 61 74 65 20 20 55 70 64 61 74 65 20
0C90 46 69 6C 65 20 4E 61 6D 65 20 2E 2E 3A 2D 20 55 70 64 61 74 65 20
0CA0 00 DF 63 21 1C 00 19 11 55 70 64 61 74 65 20 55 70 64 61 74 65 20
0CB0 00 DF 63 21 1C 00 19 11 55 70 64 61 74 65 20 55 70 64 61 74 65 20
0CC0 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E
0CD0 21 1C 00 19 11 5D C0 01 02 00 ED B0 EF 0D 52 41 02 00 ED B0 EF 0D 52 41
0CE0 4D 20 53 74 61 72 74 20 41 64 64 72 65 73 73 20 41 64 64 72 65 73 73
0CF0 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E
0D00 0C E5 DF 63 D1 E5 DF 64 E1 38 F3 2A 21 0C 22 35 E1 38 F3 2A 21 0C 22 35
0D10 0F CD 6B 0E DF 7B FE 1B CA 1C 0E 3E FF 32 01 C0 CA 1C 0E 3E FF 32 01 C0
0D20 0E 00 DF 83 20 EB 21 55 C0 06 30 DF 86 28 29 EF C0 06 30 DF 86 28 29 EF
0D30 0D 0D 20 20 20 20 20 20 20 3C 3D 3D 3D 20 46 20 3C 3D 3D 3D 20 46
0D40 49 4C 45 20 4E 4F 54 20 46 4F 55 4E 44 20 3D 3D 46 4F 55 4E 44 20 3D 3D
0D50 3D 3D 3E 00 DF 5D 18 B9 CD 23 0E 2A 61 C0 EF 20 CD 23 0E 2A 61 C0 EF 20
0D60 20 20 20 20 20 20 44 69 73 6B 20 53 63 74 6F 73 6B 20 53 63 74 6F
0D70 72 20 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E
0D80 2A 35 0F EF 0D 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20
0D90 6D 20 52 61 6D 20 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E
0DA0 2E 3A 3D 20 00 DF 66 3A C3 C0 47 EF 0D 20 20 20 20 20 20 20 20 20 20
0DB0 20 20 20 20 20 4E 75 6D 62 65 72 20 6F 66 20 53 62 65 72 20 6F 66 20 53
0DC0 65 63 74 6F 72 73 20 2E 2E 3A 3D 20 20 20 00 78 2E 3A 3D 20 20 20 00 78
0DD0 DF 68 C5 DF 5D C1 0E 00 ED 5B 61 C0 2A 35 0F DF ED 5B 61 C0 2A 35 0F DF
0DE0 82 20 03 C3 11 0D F5 EF 0D 0D 45 72 72 6F 72 20 0D 0D 45 72 72 6F 72 20
0DF0 3D 3D 3D 3E 20 00 F1 DF 88 45 6D 73 67 EF 0D 0D 88 45 6D 73 67 EF 0D 0D
0E00 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20
0E10 74 65 72 22 20 20 00 DF 7B C3 11 0D 3E FF 32 01 7B C3 11 0D 3E FF 32 01
0E20 C0 DF 5B EF 0C 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20
0E30 20 20 44 49 53 48 20 55 50 44 41 54 45 20 55 54 50 44 41 54 45 20 55 54
0E40 49 4C 49 54 59 0D 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20
0E50 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20
0E60 2D 2D 2D 2D 2D 2D 2D 2D 2D 2D 2D 2D 2D 2D 2D 2D 2D 2D 2D 2D 2D 2D
0E70 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20
0E80 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E 2E
0E90 C0 06 08 7E F7 23 10 FB EF 2E 00 06 02 7E F7 23 EF 2E 00 06 02 7E F7 23
0EA0 10 FB EF 0D 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20 20
0EB0 65 64 20 46 72 6F 6D 20 52 41 4D 20 2E 2E 2E 2E 52 41 4D 20 2E 2E 2E 2E
0EC0 3A 2D 20 00 2A 35 0F DF 66 EF 0D 20 20 20 20 66 EF 0D 20 20 20 20
0ED0 20 20 49 6E 73 65 72 74 20 44 69 73 6B 20 69 74 20 44 69 73 6B 20 69
0EE0 6E 74 6F 20 44 72 69 76 65 20 30 2E 0D 20 20 20 65 20 30 2E 0D 20 20 20
0EF0 20 20 20 20 50 72 65 73 73 20 22 45 6E 74 65 72 73 20 22 45 6E 74 65 72
0F00 22 20 77 68 65 6E 20 72 65 61 64 79 2E 0D 20 20 65 61 64 79 2E 0D 20 20
0F10 20 20 20 20 20 50 72 65 73 73 20 22 45 6E 74 65 2E 73 73 20 22 45 6E 74 65 2E
0F20 20 20 20 74 6F 20 54 65 72 6D 69 6E 61 74 65 2E 72 6D 69 6E 61 74 65 2E
0F30 3A 2D 20 00 C9 01 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0F40 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0F50 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0F60 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0F70 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

```

REVIEW OF COMPASS, ZAP, AND RAVEN ASSEMBLERS

By Stephen Weir

COMPASS (not to be confused with COMPAS Pascal), ZAP and RAVEN are tape based assemblers for use with the Nascom range of microcomputers.

COMPASS

COMPASS stands for COMPression ASSEMBler and is written by Level 9, the same people who write those excellent adventures. As you would expect therefore, COMPASS is a good quality piece of software with no bugs. It comes on a TDK cassette with a neatly printed label together with a user manual bound in the usual "Level 9 blue". On the tape there is the assembler itself within a relocator program followed by a program to convert ZEAP files to COMPASS files should you wish to change over to COMPASS from ZEAP but still use your ZEAP files. Finally, there is the source for the ZEAP convertor so that you can see how it works and also use it to try out the editing facilities.

The relocator is very useful as you can relocate COMPASS to the top of your RAM and then you have the rest of RAM from 1000h upwards free. Once relocated, COMPASS takes up about 7K. On cold start it is necessary to specify start addresses for both source file and object code (this is a default value only and can be changed at any time by an ORG directive in the source). It is also possible to specify the symbol table and workspace area. So, providing you have enough memory it should be possible to arrange things so that you can always keep the required area of memory free for the object code.

When you are testing the assembled program while the assembler is still in RAM it is often the case that the program will not do as intended and may corrupt part of the assembler. On return to COMPASS, however, a checksum routine is carried out so that if COMPASS has been corrupted a message is displayed. It is then necessary to reload COMPASS.

As I have already mentioned, COMPASS compresses the source code. It does this by using a 1-byte code for each keyword. Assembly speed is good and Level 9 claim 3000 lines per minute, but since I can't count that fast, I'll just take their word for it! During assembly, any errors are displayed on the listing and assembly is resumed (unlike ZAP80 and RAVEN which abort assembly at the first error).

Lines are entered with line numbers and editing is carried out using the NAS-SYS screen-editing. The line-numbering system adopted by Level 9 is a little strange to say the least. Firstly, numbers are in HEX and they appear on the far left of the assembly listing next to the address field. Two columns of Hex numbers make reading a little confusing. Secondly, it is not possible to enter lines in increments other than one but it is possible to insert lines using the Insert command, which also automatically numbers the inserted lines and rennumbers all succeeding lines.

Commands are entered using single characters and include listing (number of lines listed at a time may be set), string search, assemble (with listing options), and tape read, write and verify which merely use the NAS-SYS routines and therefore do not allow file names. NAS-SYS restart instructions and subroutine calls are not supported. For listing to a printer the NAS-SYS 'X' or 'U' commands have to be used.

The manual is mainly just a guide to the facilities available and does not attempt to teach the use of an assembler. This is also true of the other two manuals. However, there is some useful information given which should allow you to add extra commands to COMPASS.

ZAP

I ordered my copy of ZAP from an old copy of 80-BUS NEWS where the price quoted was \$15. When it arrived, I was pleasantly surprised to find a refund of \$8.50 since the price had since been reduced to \$6.50. It comes on a good quality BASF tape and the manual is just 11 A4 pages stapled together. ZAP takes up about 7K and also compresses the source code, so a minimum of 16K RAM is sufficient. On cold starting you are greeted with a cheery message and a report on the amount of free memory.

The editing facilities are the worst feature of this assembler and are similar to what you find on most BASICs of a few years vintage. In particular there is no pause control on listing to the screen, so you can only list in blocks of 14 lines. Also, there is no string search/change command which, together with the poor listing facilities, makes editing rather tedious. Once you have succeeded in getting the required line on the screen it can be edited using the NAS-SYS screen-editing commands. All commands must be entered in full (usually four letters). This is far less convenient than single key entry. Filenames are not used when saving the source on tape.

There are a couple of useful commands however, such as OBEY "c" where c can be any NAS-SYS command, so you can use NAS-SYS commands without having to leave the assembler. DUMP will write the assembled object code to tape and if you put a RET instruction at the end of the program, RUN will execute the program and return control to ZAP when it is finished.

ZAP also compresses the source by using 1-byte codes for mnemonics, labels and macros, and by removing all unnecessary spaces. There is no space between the line number and the label and only one space is left between label and mnemonic, and mnemonic and comment. This makes it impossible to lay out the source in neat columns for readability, since the resultant listing is always a scruffy mess!

Conditional assembly is possible by enclosing the source within an IF (expression) . . . FI statement. If the expression is evaluated to true (non-zero) then the code within the block is assembled, otherwise it is ignored and assembly continues from the line following the FI. Macros are also supported - a macro is just a group of instructions which is given a suitable name. When the macro name appears in the source, the assembler inserts the machine code which makes up the macro. A macro would normally be used where a small group of instructions are used many times, but where a subroutine would not be appropriate. For example a macro to push registers at the start of each subroutine. Parameters may be passed to the macro so that the same macro may operate on different data, which may be registers, labels, expressions, etc. In the ZAP assembly listing the macro appears in full with all the mnemonics, so that it is still readable by anyone not familiar with macros. Labels and macros may be listed at any time but if you have deleted any from the source during programming, they still appear in the tables and it is impossible to get rid of them! Assembly is aborted at the first error, however the error messages given are reasonably full and clear.

Multistatement lines may be used with mnemonics separated by colons. The NAS-SYS restart mnemonics are supported (although BRKPT and RDEL are incorrectly represented by BREAK and KDEL respectively). In addition, "for convenience" as the manual puts it, there are alternative mnemonics for some of the more common instructions. e.g. CLA (clear acc) for XOR A, JSR (jump to subroutine) for CALL. I really don't see the point of including alternatives since their use will only lead to lack of standardisation and the Zilog mnemonics are quite logical and clear enough. Another unusual, but more interesting feature is the inclusion of the Z80 unknown opcodes, or most of them anyway.

RAVEN

At #30, RAVEN is the most expensive of the three and about the same price as ZEAP. However, when you see the sort of things it can do, I'm sure that you will agree that it is an excellent piece of software and quite reasonably priced at that. The TDK tape has the assembler itself on one side and the Z80 macro library on the other (see later for an explanation of the macro library). The manual is a loose-leaf ring-binder and has been printed directly by a dot-matrix printer. Unfortunately, the paper used is a bit thin and the holes are punched close to the edge so after a few hours' use the holes tear and you literally have a loose-leaf manual! Raven takes up about 16K of memory and it does not perform any text compression (apart from the use of TABs to save spaces), so 32K RAM is needed even for a fairly small program.

It is possible to interface RAVEN to your own system, in particular a printer routine, optional form feeds, cursor character code and repeat speed, and finally the start address for the source code. The adapted version may then be saved on tape and used as the working copy. The many facilities available on this assembler are well explained with clear examples. The only ambiguity I came across was the procedure for attaching your own printer routine. It didn't work for me so I had to devise my own method.

The editor is one of the best features of RAVEN. It is a full screen editor and I have found it to be far superior to the usual line-based editor that is supplied. The screen is best thought of as a 48 character by 15 line window on the text file, which is moved around the file by the cursor keys. Because any part of the file may be viewed at any time just by using the cursor keys, line numbers are not necessary. Lines can be up to 255 characters long but this is probably too long for most uses. An option of 80 (or 132) characters would be useful since this would be the same as most printers.

There is a comprehensive range of commands, selected by a single letter:- change and insert text, find and replace strings, delete and copy lines or blocks of text. As well as read, write and verifying of named files, it is possible to join a file from tape to one already in memory so that you can build up the source program from library files (for example). Tabs may be positioned anywhere along the 255 character line width and you may have as many as you wish. If you forget the function of a particular key, the Help command will give a short description. However it is usually necessary to consult the manual anyway since most commands have several options which require further input. It was while I was playing around with the help facility that I discovered a command that was not mentioned in the manual, this was to "update tabs". I've been trying it out but it doesn't seem to do anything useful!

The top line (line 16) is used to display information to the user. Cursor position is given by column number (1 to 255) but I would also like to have seen line position so that you would have a better idea of what part of the file you are at. This display may be changed to give the length of the file in bytes. Other information is displayed during tape transfers, and string search/replace etc.

Whereas assemblers for micros usually have one fixed instruction set, RAVEN does not have any instruction set built in at all. Before it can be used as an assembler it has to be supplied with the instruction set for the particular CPU. All instructions and most psuedo-ops are defined as macros. The list of macros defining the instruction set is treated just like any other source file, however for normal use it can be incorporated into the assembler semi-permanently so that it is loaded into RAM along with the assembler. The instruction set supplied is, of course, for the Z80 but you could just as easily replace it with say a 6502 set (although you would have to write this

yourself). RAVEN then becomes a cross-assembler for the 6502. Other uses for this powerful macro facility would be to add extra instructions to the Z80, e.g. LD (HL),HL etc. Or if you are involved in a particular field of interest such as robotics, music or graphics, then you could write your own personal programming language where each program instruction would in fact be a macro name. RAVEN then effectively becomes a compiler for your new language.

One notable omission from the Z80 library was the NAS-SYS restart instructions. They can be easily added by writing a few extra macros using the methods described very clearly in the manual. The comma in the jump conditional instructions was missing, and as I was used to putting it in, I altered the macros accordingly. Another much more serious error was only discovered after spending many hours puzzling over apparently perfect programs which would just keep on crashing. The problem was that the assembler was allowing relative jumps up to +/- 255 instead of +129 to -126. So, for instance, if the jump was between +130 and +255 the error would not be detected and the byte put into the object code would in fact work out to be a negative jump and the program would therefore crash. I managed to overcome this problem by rewriting the macro definition dealing with relative jumps. Being rather surprised that this problem had not been spotted sooner, I wrote to the author, so he is now aware of it.

At first you might think that there has to be a macro definition for every possible permutation of instruction (about 750 I think), but RAVEN provides facilities for much more concise definitions so that each instruction "type" (e.g. 8-bit register load) may be defined by one macro. The parameters supplied to the macro determine the machine code generated. It is necessary to read the manual to fully understand the method, but I will give an example which may give some idea. e.g. the macro to define 8-bit register to register loads:

```
DEFMAC ("LD*,*",R8,R8)
      DB 40H + #0*8 + #1
END.
```

"LD , " is the name of the macro. The asterixes show the position that the parameters must be in the macro name (separated by a comma in this case). R8 stipulates that the parameters must belong to a previously defined set, R8, which is a set of the 8-bit register names A to L and A is assigned a value of 7, B a value of 0 etc. DB is the define byte pseudo-op. #0 and #1 refer to the first and second parameters respectively. The expression thus generates the required bit pattern according to the registers specified (see the Z80 Technical Manual for a breakdown of the bit patterns). e.g. for LD A,B the expression is evaluated like so:

40H:	01000000
(Reg A = 7) #0*8:	00111000
(Reg B = 0) #1 :	00000000

Total	01111000 = 78H

There are also several PASCAL-like control structures for the generation of loops within macros. These may be used for the production of data tables. e.g. a macro to clear a block of memory of any size:

```

DEFMAC ("CLEAR * BYTES", NUM) ;NUM is a set of integers
                                ; 0 - 65535
    COUNTER = #0                ;Loop counter set to
                                ;number of bytes
    $WHILE COUNTER > 0
    DB 0
    COUNTER = COUNTER - 1
    END
END.

```

So the macro call CLEAR 24 BYTES will set the next 24 bytes to zero. Notice that there may be spaces within the macro name thus increasing legibility. Other constructs are IF THEN .. ELSE , and \$REPEAT .. UNTIL. Expression handling is comprehensive with 18 operators and 4 number bases to work with.

As well as the usual assembler options you can also specify user defined options for such things as conditional assembly. i.e. the value of the boolean argument in an IF .. THEN block may be set by an option at assembly time. Assembly stops at the first error with a single word error message which is not always very helpful. RAVEN is rather slower than the other two assemblers but this is to be expected due to its design. I had the idea to add all the EQUATES that I use (e.g. port addresses, video ram addresses, keyboard control codes) to the permanent macro library to save having to put any in at the beginning of a program. While this worked perfectly OK, the macro library was by now so large that assembly time was painfully long. But I am using a 2 MHz NASCOM 1 so the speed is obviously half of what it could be.

Conclusion

So there we are, three more assemblers for the NASCOM. It would be a little unfair to say which is best since prices vary. Each has its good points - COMPASS is very fast, ZAP offers a macro/conditional assembler at a very low price, and RAVEN can be used very much depending on your own imagination.

The three assemblers are supplied by:

COMPASS v1.3	Level 9 Computing 229 Hughendon Rd High Wycombe Bucks. HP13 5PG	#12.00
ZAP v1.6	Syrtis Software 23 Quantock Rd Bridgewater Somerset TA6 7EG	#6.50
RAVEN	P. Harvey 30 Jericho St Oxford OX2 6BU	#30.00

RANDOM RUMOURS (& TRUTHS?)**By S. Monger**

Ah well, I've never claimed to be perfect, have I? In the last issue I mentioned the Gemini GM886 board containing an iAPX186. Well it seems that this idea was dropped a while ago, and in fact it is the GM888 that you will see in coming months. What is the difference - well the GM886 board was to be a board interfaced to the 80-BUS via a couple of I/O ports (like the GM812 IVC, GM832 SVC and IO828 Pluto). It contained its own 256K RAM and consequently would have been very fast, and expensive! It would also have had the disadvantage of not being able to drive any I/O peripherals directly, such as Pluto, and would have to have passed on the task via the Z80 on the host processor board, consequently slowing down the operation quite considerably. After a little research Gemini discovered that they could produce a board that would drive the 80-BUS directly, and thus be able to use all the I/O on the bus. More importantly, from a cost viewpoint, the board would be able to have NO memory on it, and would therefore almost certainly be under #200. Of course in interfacing to the 80-BUS the processor used has had to be changed to one with an external 8-bit interface, and so the 8MHz 8088 has been chosen.

So how do you use it? Well the system boots as normal under the Z80, you then flip a bit of an 80-BUS I/O port, the 8088 puts out a bus request, the Z80 finishes the instruction that it was executing, and then the 8088 takes over control of the entire system. If at any stage you wish to pass control back to the Z80 then the 8088 flips the I/O bit back, and the Z80 carries on where it left off. In theory, therefore, you can write code that is of mixed type, and switch to and fro between processors - must be of some use!

The GM888 board will also contain another of those blasted Real Time Clock thingies, with battery back-up. This serves two purposes. First of all certain 16 bit operating systems allow time and date stamping of files, and secondly the actual chip chosen provides oodles of interrupts if required, and this is necessary for task switching with Concurrent-DOS (previously called Concurrent-CP/M) and presumably with Multi-tasking MS-DOS. There is also a socket for the 8087 high speed arithmetic co-processor. Board availability? Don't know!!

And how about the extra memory that the 8088 can support? Well Gemini have launched a 256K RAM board that supports the 80-BUS extended address lines. It has several modes of operation:

- 1) 4 pages of 64K from address 0 (for use with Nascom 2s and Gemini GM811s).
- 2) 4 pages of 64K from any extended address (EA).
- 3) 1 page (any) of 256K from any extended address.
- 4) 3 pages of 64K from 0 and 1 page of 64K from EA1 (allows GM813 users to have 4 pages of 64K at 0 without contention).

In addition to the above there is a 'Common Area' mode which puts a common area of memory (selectable between 4K and 8K) in all four pages.

With all these modes it is possible to set up virtually any permutation of page mode and extended addressing that any one may want, and systems with the GM813 or GM888 will accept up to 8 boards = 2Mbytes! (4 pages of 512K.) And the price of all this flexibility ? - #325 + VAT. Available now!!

World Domination!

With a distinct lack of signs of activity from Lucas, Gemini continues its 80-BUS domination campaign! The Climax vector graphics colour board has been taken over by Gemini, as the GM837. It is now only available in its fully populated form (modulated and R-G-B outputs) and the price has been reduced to #165 + VAT. Gemini has also taken over the IO Research 8-bit A-D board, now the GM824, and the price remains the same at #125 + VAT. One 80-BUS price has risen, the Belectra arithmetic board. It is now a totally horrendous #268 + VAT!!

Laurence lends a helping hand.



Laurence's train of thought is derailed by the telephone.

Grrt, mumble, IF... THEN, mumble, mumble SETNEW, grunt.



Ring, ring.
Ring, ring.

In the interest of good taste, this section of profane verbiage has been censored by the editor.



Laurence decides to lumber Heloise Fortran with the problem while he attends to more important things....

THE PIG AND ELEPHANT! + ## = PO*!!@ as a neut.

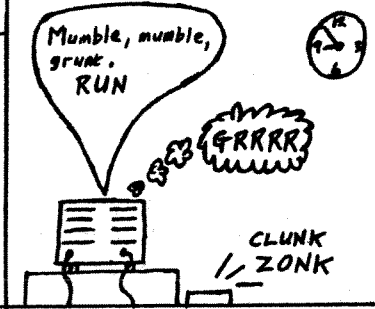
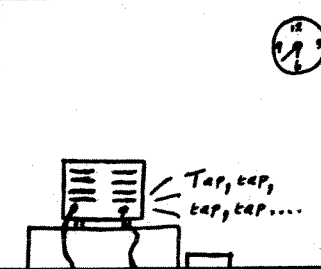
.... But.



Like all good programmers, our hero thinks the problem through first....



.... then starts writing the program.



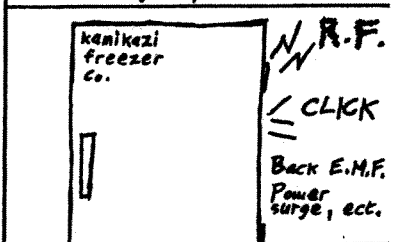
Part of the fun of computing is de-bugging your software....



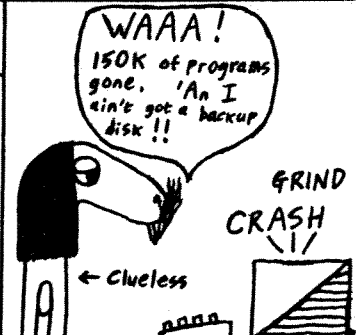
.... which in most cases is easier said than done!



Meanwhile in the kitchen, the refrigerator's thermostat decides it's time to go 'ape'.



Laurence is unaware of the miasma borne mayhem heading his way.



It's always a good idea to make backup copies of your disks in case you get caught out like Laurence.

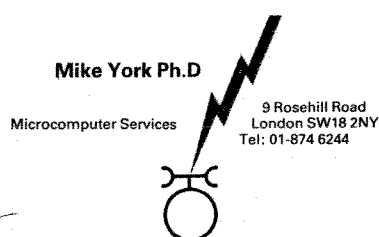
By D.G. Richards.
Tonyretail. Mid. Glamorgan.
1983.

ADS

Ram-A board, 32k RAM, 2716 conversion (no EPROMs). With INMC mods, but still won't work at 4MHz without waits (dunno why...) #45 ono. HSLN twin-drive digital cassette system. As reviewed 80-Bus News Vol 1 iss. 4. With original ROMs (2708s, assembled for #D000 and ports #F8 to #FF) or my enhanced OS (2716 - assembled for #D000, and ports #78 to #7F); 4118s, connecting leads and some tapes. #170 ono. Adrian Perkins. Tel. Bracknell (0344) 485816

Nascom 1, port select corrected, card frame, buffer PCB, 32K RAM fully debugged, PSU, 4800 baud cassette interface, TTY interface, keyboard, Harris terminal with 12" Motorola VDU, TTY printer, BASIC & assembler on tape. All documentation. #140 the lot or prepared to split. Jim Taylor Boldon 36215.

Floppy disk system. GM815/2, GM809 etc. Twin Pertec drives (350K per drive) in cabinet with power supply. Double density controller card, CP/MZ 2.2 operating system, and lots of software including Pascal, BASIC and C languages, Utilities, Adventure, Games and Business software. Totally reliable system. #450. Also, twin single sided drives in cabinet with power supply and Henelec Singler Density Controller. Including all above software and Vers. 2.2 operating system. Haggle around #275. Call John on 01-380-0191 between 10am and 7.30pm.

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The HSA-88B is easily used from within assembly language programs. High level language programs require a compiler with modified run-time

routines. We are offering with every HSA-88B a FREE latest Hisoft HP5 Pascal compiler which has been specially adapted to compile HSA-88B-oriented code. This compiler is already extremely fast and with the HSA-88B it outperforms all other Z80 Pascal compilers, in many cases by an order of magnitude. The standard Pascal variable types plus 32 bit integers (ideal for financial applications) are supported together with a full range of maths functions rarely seen in Pascals or Basics. The size of the run-time routines is greatly reduced over other compilers because the HSA-88B performs the arithmetic functions in hardware.

The complete package consists of the HSA-88B processor card, HP5 compiler on Gemini 5¼" DSDD disc (other formats available including Nascom 5¼" and IBM 8" SSD) and HSA-88B and HP5 documentation and programming examples. Package price £268 plus VAT, UK postage free. Not suitable for Nascom 1.

BELECTRA LTD. 11 Decoy Road, Worthing, West Sussex BN14 8ND Telephone 0903-213131

NEW SUPER DISKPEN (PENVG:3)

DISKPEN has been rewritten and revised. This popular text editor/formatter now includes a 'HELP' facility, and new features for the print control of the most popular printers, underline, bold, etc (also user patchable for the less popular types). New features include block delete, better move commands, new cursor control, optional hyphenation, visible indentation setting and lots more. A major enhancement is the ability to handle overlay files so that PEN can use auxilliary packages such as the MAXIFILE free field file searching utility or the print spooling utility.

The new DISKPEN is useable on all Gemini multiboard computers (Galaxy, Kenilworth, Quantum) and Nascom/Gemini hybrids, (MAPPEN is available for

users of the MAP video card). DISKPEN is available as an upgrade to earlier DISKPENS and GEMPENS at 17.25 inc. VAT., or to new purchasers at 57.50 inc. VAT. (Please state disk format when ordering.)

MAXIFILE overlay 23.00 inc. VAT (20.00 + VAT)

SPoolER overlay 17.25 inc. VAT (15.00 + VAT)

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ALLDISC is a new approach to the problem of lots of different machines all with different disk formats. Designed for use with the Gemini and Nascom CP/M computers, ALLDISC replaces the existing disk drivers and the CP/M disk parameter headers from an archive of different formats. Up to 6 drives and two controller cards are supported, the drives may be a mixture of 8", 5.25 or 3.5" types in either single or double density and may be single or double sided. 96 tpi 5.25" drives can be made to 'double step' to read and write 48 tpi disks. The Gemini GM833 512K virtual disk RAM is also supported. All this adds up to a very powerful system where the drives fitted to the system may be reconfigured to read and write disks of different formats. The limitations are that the disks must be to IBM3740 CRC standards and must be soft sector (or in other words, it will cope with most disks).

The archive is supplied with nine useful formats when supplied, with others being made available to registered users free of charge for the first six months from registration. The archive can be edited and new formats created. Disks can be formatted from the archive so there is no need for preformatted disks when transferring software.

ALLDISC is supplied with full documentation and hints and pointers to discovering unknown disk formats. ALLDISC costs 172.50 inc VAT (150.00 + VAT). Carriage & packing 50p

BDOSZ

Yes, you guessed it. Some enterprising person has now 'disconbooberated' the BDOS in CP/M and rewritten it as a Z80 program. Its fully compatible with the original with no bugs found to date. Because it's written in Z80 code it's smaller, this has allowed room for tidying up all the annoying stupidities in the original BDOS so that errors like:

BDOS ERROR ON x: R/O

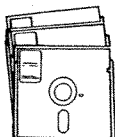
which usually causes you to lose everything you've just done, becomes the far more helpful:

Disk x: is set R/O

Do it anyway? (Y/N/^C)

Which, of course, means you don't lose anything. It even allows you to change disks when they are full without loss of data. In all, a lovely piece of software. Available in most popular 5.25" formats (please state when ordering) at 11.50 inc. VAT.

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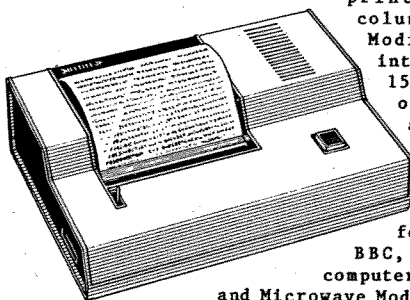
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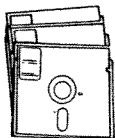
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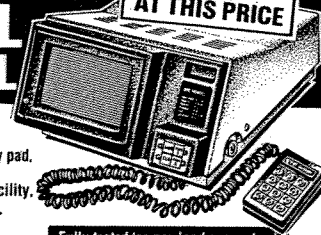
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MDIS is a CP/M disassembler useable on most standard CP/M machines, switchable for either 8080 or Z80 mnemonics. Command syntax is designed to be similar to the Microsoft M80 assembler, and redirected input from .DAT files is allowed. The output files to printer or disk may include the disassembled code or in M80 format and output label types are differentiated by different letter prefixes. A powerful package at a price which is a lot less than its competitors. Supplied in most popular 5.25" disk formats (state type when ordering) at 57.50 inc. VAT. Carriage & packing 50p

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